

Cryptographic software engineering, part 2

Daniel J. Bernstein

Previous part:

- General software engineering.
- Using const-time instructions.

1

Software optimization

Almost all software is
much slower than it could be.

2

Cryptographic software engineering, part 2

Daniel J. Bernstein

Previous part:

- General software engineering.
- Using const-time instructions.

1

Software optimization

Almost all software is
much slower than it could be.

Is software applied to much data?

Usually not. Usually the
wasted CPU time is negligible.

2

Cryptographic software engineering, part 2

Daniel J. Bernstein

Previous part:

- General software engineering.
- Using const-time instructions.

1

Software optimization

Almost all software is
much slower than it could be.

Is software applied to much data?

Usually not. Usually the
wasted CPU time is negligible.

But *crypto software* should be
applied to all communication.

Crypto that's too slow

⇒ fewer users

⇒ fewer cryptanalysts

⇒ less attractive for everybody.

2

graphic
engineering,

. Bernstein

s part:

al software engineering.

const-time instructions.

1

Software optimization

Almost all software is
much slower than it could be.

Is software applied to much data?

Usually not. Usually the
wasted CPU time is negligible.

But *crypto software* should be
applied to all communication.

Crypto that's too slow

⇒ fewer users

⇒ fewer cryptanalysts

⇒ less attractive for everybody.

2

Typical s

X is a c

You hav

reference

You wan

software

as effici

You hav

(Can rep

You mea

impleme

1

Software optimization

Almost all software is much slower than it could be.

Is software applied to much data?

Usually not. Usually the wasted CPU time is negligible.

But *crypto software* should be applied to all communication.

Crypto that's too slow

⇒ fewer users

⇒ fewer cryptanalysts

⇒ less attractive for everybody.

2

Typical situation:

X is a cryptographic

You have written a reference implementation

You want (constant-time) software that computes as efficiently as possible

You have chosen a (Can repeat for other)

You measure performance of implementation. M

1

Software optimization

Almost all software is much slower than it could be.

Is software applied to much data?

Usually not. Usually the wasted CPU time is negligible.

But *crypto software* should be applied to all communication.

Crypto that's too slow

⇒ fewer users

⇒ fewer cryptanalysts

⇒ less attractive for everybody.

2

Typical situation:

X is a cryptographic system

You have written a (const-time) reference implementation of

You want (const-time) software that computes X as efficiently as possible.

You have chosen a target CPU
(Can repeat for other CPUs.)

You measure performance of implementation. Now what?

Software optimization

Almost all software is much slower than it could be.

Is software applied to much data?

Usually not. Usually the wasted CPU time is negligible.

But *crypto software* should be applied to all communication.

Crypto that's too slow

⇒ fewer users

⇒ fewer cryptanalysts

⇒ less attractive for everybody.

Typical situation:

X is a cryptographic system.

You have written a (const-time) reference implementation of X .

You want (const-time) software that computes X as efficiently as possible.

You have chosen a target CPU.
(Can repeat for other CPUs.)

You measure performance of the implementation. Now what?

Performance optimization

All software is

slower than it could be.

Why are we applied to much data?

Why not. Usually the

overhead CPU time is negligible.

Opto software should be

close to all communication.

Why that's too slow

for users

for cryptanalysts

is attractive for everybody.

2

Typical situation:

X is a cryptographic system.

You have written a (const-time) reference implementation of X .

You want (const-time) software that computes X as efficiently as possible.

You have chosen a target CPU. (Can repeat for other CPUs.)

You measure performance of the implementation. Now what?

3

A simplified

Target CPU

microcontroller

one ARM

Reference

```
int sum
```

```
{
```

```
    int r
```

```
    int i
```

```
    for (i
```

```
        res
```

```
    return
```

```
}
```


2

Typical situation:

X is a cryptographic system.

You have written a (const-time) reference implementation of X .

You want (const-time) software that computes X as efficiently as possible.

You have chosen a target CPU.
(Can repeat for other CPUs.)

You measure performance of the implementation. Now what?

3

A simplified example

Target CPU: TI LM32 microcontroller code on one ARM Cortex-M

Reference implementation

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i <
        result += x[
    return result;
}
```

2

Typical situation:

X is a cryptographic system.

You have written a (const-time) reference implementation of X .

You want (const-time) software that computes X as efficiently as possible.

You have chosen a target CPU.
(Can repeat for other CPUs.)

You measure performance of the implementation. Now what?

3

A simplified example

Target CPU: TI LM4F120H5 microcontroller containing one ARM Cortex-M4F core.

Reference implementation:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += x[i];
    return result;
}
```

Typical situation:

X is a cryptographic system.

You have written a (const-time) reference implementation of X .

You want (const-time) software that computes X as efficiently as possible.

You have chosen a target CPU.
(Can repeat for other CPUs.)

You measure performance of the implementation. Now what?

A simplified example

Target CPU: TI LM4F120H5QR microcontroller containing one ARM Cortex-M4F core.

Reference implementation:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += x[i];
    return result;
}
```

situation:

cryptographic system.

we written a (const-time)
implementation of X .

at (const-time)

that computes X

as fast as possible.

we chosen a target CPU.

(beat for other CPUs.)

measure performance of the

implementation. Now what?

3

A simplified example

Target CPU: TI LM4F120H5QR
microcontroller containing
one ARM Cortex-M4F core.

Reference implementation:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += x[i];
    return result;
}
```

4

Counting

```
static v
```

```
    *const
```

```
    = (vo
```

```
    ...
```

```
int bef
```

```
int res
```

```
int aft
```

```
UARTpri
```

```
    resul
```

Output s

Change

3

A simplified example

Target CPU: TI LM4F120H5QR
microcontroller containing
one ARM Cortex-M4F core.

Reference implementation:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += x[i];
    return result;
}
```

4

Counting cycles:

```
static volatile
    *const DWT_CYC
    = (void *) 0xE
...

```

```
int beforesum =
int result = sum
int aftersum = *
UARTprintf("sum
    result, aftersu

```

Output shows 801

Change 1000 to 50

3

A simplified example

Target CPU: TI LM4F120H5QR
microcontroller containing
one ARM Cortex-M4F core.

Reference implementation:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += x[i];
    return result;
}
```

4

Counting cycles:

```
static volatile unsigned
    *const DWT_CYCCNT
    = (void *) 0xE0001004;
...
```

```
int beforesum = *DWT_CYCCNT;
int result = sum(x);
int aftersum = *DWT_CYCCNT;
UARTprintf("sum %d %d\n",
    result, aftersum - beforesum);
```

Output shows 8012 cycles.
Change 1000 to 500: 4012.

A simplified example

Target CPU: TI LM4F120H5QR
microcontroller containing
one ARM Cortex-M4F core.

Reference implementation:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += x[i];
    return result;
}
```

Counting cycles:

```
static volatile unsigned int
    *const DWT_CYCCNT
    = (void *) 0xE0001004;
...

int beforesum = *DWT_CYCCNT;
int result = sum(x);
int aftersum = *DWT_CYCCNT;
UARTprintf("sum %d %d\n",
    result, aftersum - beforesum);
```

Output shows 8012 cycles.

Change 1000 to 500: 4012.

modified example

CPU: TI LM4F120H5QR

controller containing

M Cortex-M4F core.

ce implementation:

```
(int *x)
```

```
result = 0;
```

```
;
```

```
for (i = 0; i < 1000; ++i)
```

```
    result += x[i];
```

```
return result;
```

4

Counting cycles:

```
static volatile unsigned int
```

```
    *const DWT_CYCCNT
```

```
    = (void *) 0xE0001004;
```

```
...
```

```
int beforesum = *DWT_CYCCNT;
```

```
int result = sum(x);
```

```
int aftersum = *DWT_CYCCNT;
```

```
UARTprintf("sum %d %d\n",
```

```
    result, aftersum - beforesum);
```

Output shows 8012 cycles.

Change 1000 to 500: 4012.

5

“Okay, &

Um, are

really th

ole

M4F120H5QR

ntaining

M4F core.

entation:

;

1000; ++i)

i];

4

Counting cycles:

```
static volatile unsigned int
```

```
    *const DWT_CYCCNT
```

```
    = (void *) 0xE0001004;
```

```
    ...
```

```
int beforesum = *DWT_CYCCNT;
```

```
int result = sum(x);
```

```
int aftersum = *DWT_CYCCNT;
```

```
UARTprintf("sum %d %d\n",
```

```
    result, aftersum - beforesum);
```

Output shows 8012 cycles.

Change 1000 to 500: 4012.

5

“Okay, 8 cycles pe

Um, are microcont

really this slow at

4

Counting cycles:

```
static volatile unsigned int
    *const DWT_CYCCNT
    = (void *) 0xE0001004;
...

int beforesum = *DWT_CYCCNT;
int result = sum(x);
int aftersum = *DWT_CYCCNT;
UARTprintf("sum %d %d\n",
    result, aftersum-beforesum);
```

Output shows 8012 cycles.

Change 1000 to 500: 4012.

5

“Okay, 8 cycles per addition
Um, are microcontrollers
really this slow at addition?”

Counting cycles:

```
static volatile unsigned int
    *const DWT_CYCCNT
    = (void *) 0xE0001004;
...

int beforesum = *DWT_CYCCNT;
int result = sum(x);
int aftersum = *DWT_CYCCNT;
UARTprintf("sum %d %d\n",
    result, aftersum-beforesum);
```

Output shows 8012 cycles.

Change 1000 to 500: 4012.

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Counting cycles:

```
static volatile unsigned int
    *const DWT_CYCCNT
    = (void *) 0xE0001004;
...

int beforesum = *DWT_CYCCNT;
int result = sum(x);
int aftersum = *DWT_CYCCNT;
UARTprintf("sum %d %d\n",
    result, aftersum-beforesum);
```

Output shows 8012 cycles.

Change 1000 to 500: 4012.

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Bad practice:

Apply random “optimizations”
(and tweak compiler options)
until you get bored.
Keep the fastest results.

Counting cycles:

```
static volatile unsigned int
    *const DWT_CYCCNT
    = (void *) 0xE0001004;
...

int beforesum = *DWT_CYCCNT;
int result = sum(x);
int aftersum = *DWT_CYCCNT;
UARTprintf("sum %d %d\n",
    result, aftersum-beforesum);
```

Output shows 8012 cycles.

Change 1000 to 500: 4012.

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Bad practice:

Apply random “optimizations”
(and tweak compiler options)
until you get bored.

Keep the fastest results.

Try -Os: 8012 cycles.

Counting cycles:

```
static volatile unsigned int
    *const DWT_CYCCNT
    = (void *) 0xE0001004;
...

int beforesum = *DWT_CYCCNT;
int result = sum(x);
int aftersum = *DWT_CYCCNT;
UARTprintf("sum %d %d\n",
    result, aftersum-beforesum);
```

Output shows 8012 cycles.

Change 1000 to 500: 4012.

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Bad practice:

Apply random “optimizations”
(and tweak compiler options)
until you get bored.

Keep the fastest results.

Try -Os: 8012 cycles.

Try -O1: 8012 cycles.

Counting cycles:

```
static volatile unsigned int
    *const DWT_CYCCNT
    = (void *) 0xE0001004;
...

int beforesum = *DWT_CYCCNT;
int result = sum(x);
int aftersum = *DWT_CYCCNT;
UARTprintf("sum %d %d\n",
    result, aftersum-beforesum);
```

Output shows 8012 cycles.

Change 1000 to 500: 4012.

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Bad practice:

Apply random “optimizations”
(and tweak compiler options)
until you get bored.

Keep the fastest results.

Try -Os: 8012 cycles.

Try -O1: 8012 cycles.

Try -O2: 8012 cycles.

Counting cycles:

```
static volatile unsigned int
    *const DWT_CYCCNT
    = (void *) 0xE0001004;
...

int beforesum = *DWT_CYCCNT;
int result = sum(x);
int aftersum = *DWT_CYCCNT;
UARTprintf("sum %d %d\n",
    result, aftersum-beforesum);
```

Output shows 8012 cycles.

Change 1000 to 500: 4012.

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Bad practice:

Apply random “optimizations”
(and tweak compiler options)
until you get bored.

Keep the fastest results.

Try -Os: 8012 cycles.

Try -O1: 8012 cycles.

Try -O2: 8012 cycles.

Try -O3: 8012 cycles.

g cycles:

```
volatile unsigned int  
t DWT_CYCCNT  
id *) 0xE0001004;
```

```
oresum = *DWT_CYCCNT;  
ult = sum(x);  
ersum = *DWT_CYCCNT;  
ntf("sum %d %d\n",  
t, aftersum-beforesum);
```

shows 8012 cycles.

1000 to 500: 4012.

5

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Bad practice:

Apply random “optimizations”
(and tweak compiler options)
until you get bored.
Keep the fastest results.

Try -0s: 8012 cycles.

Try -01: 8012 cycles.

Try -02: 8012 cycles.

Try -03: 8012 cycles.

6

Try mov

```
int sum  
{  
    int r  
    int i  
    for (  
        resu  
    return  
}
```

```
unsigned int
CNT
0001004;

*DWT_CYCCNT;
(x);
DWT_CYCCNT;
%d %d\n",
m-beforesum);

2 cycles.
00: 4012.
```

5

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Bad practice:

Apply random “optimizations”
(and tweak compiler options)
until you get bored.
Keep the fastest results.

Try -Os: 8012 cycles.
Try -O1: 8012 cycles.
Try -O2: 8012 cycles.
Try -O3: 8012 cycles.

6

Try moving the po
int sum(int *x)
{
 int result = 0
 int i;
 for (i = 0; i <
 result += *x
 return result;
}

5

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Bad practice:

Apply random “optimizations”
(and tweak compiler options)
until you get bored.
Keep the fastest results.

Try -Os: 8012 cycles.

Try -O1: 8012 cycles.

Try -O2: 8012 cycles.

Try -O3: 8012 cycles.

6

Try moving the pointer:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += *x++;
    return result;
}
```

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Bad practice:

Apply random “optimizations”
(and tweak compiler options)
until you get bored.
Keep the fastest results.

Try -O0: 8012 cycles.

Try -O1: 8012 cycles.

Try -O2: 8012 cycles.

Try -O3: 8012 cycles.

Try moving the pointer:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += *x++;
    return result;
}
```

“Okay, 8 cycles per addition.
Um, are microcontrollers
really this slow at addition?”

Bad practice:

Apply random “optimizations”
(and tweak compiler options)
until you get bored.
Keep the fastest results.

Try -Os: 8012 cycles.

Try -O1: 8012 cycles.

Try -O2: 8012 cycles.

Try -O3: 8012 cycles.

Try moving the pointer:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += *x++;
    return result;
}
```

8010 cycles.

3 cycles per addition.
microcontrollers
is slow at addition?"

Practice:
random "optimizations"
break compiler options)
you get bored.
the fastest results.

: 8012 cycles.
: 8012 cycles.
: 8012 cycles.
: 8012 cycles.

6

Try moving the pointer:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += *x++;
    return result;
}
```

8010 cycles.

7

Try coun

```
int sum
{
    int re
    int i
    for (
        resu
    return
}
```

6

Try moving the pointer:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += *x++;
    return result;
}
```

8010 cycles.

7

Try counting down

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 1000;
        result += *x;
        i--;)
    return result;
}
```

6

Try moving the pointer:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += *x++;
    return result;
}
```

8010 cycles.

7

Try counting down:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 1000; i > 0; --i)
        result += *x++;
    return result;
}
```


Try moving the pointer:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += *x++;
    return result;
}
```

8010 cycles.

Try counting down:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 1000; i > 0; --i)
        result += *x++;
    return result;
}
```

Try moving the pointer:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; ++i)
        result += *x++;
    return result;
}
```

8010 cycles.

Try counting down:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 1000; i > 0; --i)
        result += *x++;
    return result;
}
```

8010 cycles.

ing the pointer:

```
(int *x)

result = 0;

;

i = 0; i < 1000; ++i)

result += *x++;

n result;
```

cles.

7

Try counting down:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 1000; i > 0; --i)
        result += *x++;
    return result;
}
```

8010 cycles.

8

Try using

```
int sum
{
    int r
    int *y
    while
        res
    return
}
```

printer:

```
;
1000; ++i)
++;
```

7

Try counting down:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 1000; i > 0; --i)
        result += *x++;
    return result;
}
```

8010 cycles.

8

Try using an end p

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1;
    while (x != y)
        result += *x;
    return result;
}
```

7

Try counting down:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 1000; i > 0; --i)
        result += *x++;
    return result;
}
```

8010 cycles.

8

Try using an end pointer:

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    while (x != y)
        result += *x++;
    return result;
}
```

Try counting down:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 1000; i > 0; --i)
        result += *x++;
    return result;
}
```

8010 cycles.

Try using an end pointer:

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    while (x != y)
        result += *x++;
    return result;
}
```

Try counting down:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 1000; i > 0; --i)
        result += *x++;
    return result;
}
```

8010 cycles.

Try using an end pointer:

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    while (x != y)
        result += *x++;
    return result;
}
```

8010 cycles.

Counting down:

```
(int *x)
{
    int result = 0;
    ;
    for (i = 1000; i > 0; --i)
        result += *x++;
    return result;
}
```

cycles.

8

Try using an end pointer:

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    while (x != y)
        result += *x++;
    return result;
}
```

8010 cycles.

9

Back to

```
int sum
{
    int r
    int i
    for (
        resu
        resu
    }
    return
}
```


8

Try using an end pointer:

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    while (x != y)
        result += *x++;
    return result;
}
```

8010 cycles.

9

Back to original.

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i++)
        result += x[i];
    return result;
}
```

8

Try using an end pointer:

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    while (x != y)
        result += *x++;
    return result;
}
```

8010 cycles.

9

Back to original. Try unrolli

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i++)
        result += x[i];
    return result;
}
```

Try using an end pointer:

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    while (x != y)
        result += *x++;
    return result;
}
```

8010 cycles.

Back to original. Try unrolling:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 2) {
        result += x[i];
        result += x[i + 1];
    }
    return result;
}
```

Try using an end pointer:

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    while (x != y)
        result += *x++;
    return result;
}
```

8010 cycles.

Back to original. Try unrolling:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 2) {
        result += x[i];
        result += x[i + 1];
    }
    return result;
}
```

5016 cycles.

g an end pointer:

```
(int *x)
{
    result = 0;
    y = x + 1000;
    while (x != y)
        result += *x++;
    return result;
}
```

cles.

9

Back to original. Try unrolling:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 2) {
        result += x[i];
        result += x[i + 1];
    }
    return result;
}
```

5016 cycles.

10

```
int sum
{
    int re
    int i
    for (
        resu
        resu
        resu
        resu
        resu
    }
    return
}
```

pointer:

;

000;

++;

9

Back to original. Try unrolling:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 2) {
        result += x[i];
        result += x[i + 1];
    }
    return result;
}
```

5016 cycles.

10

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i <
        result += x[
        result += x[
        result += x[
        result += x[
        result += x[
    }
    return result;
}
```

9

Back to original. Try unrolling:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 2) {
        result += x[i];
        result += x[i + 1];
    }
    return result;
}
```

5016 cycles.

10

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 1) {
        result += x[i];
        result += x[i + 1];
        result += x[i + 2];
        result += x[i + 3];
        result += x[i + 4];
    }
    return result;
}
```

Back to original. Try unrolling:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 2) {
        result += x[i];
        result += x[i + 1];
    }
    return result;
}
```

5016 cycles.

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 5) {
        result += x[i];
        result += x[i + 1];
        result += x[i + 2];
        result += x[i + 3];
        result += x[i + 4];
    }
    return result;
}
```


Back to original. Try unrolling:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 2) {
        result += x[i];
        result += x[i + 1];
    }
    return result;
}
```

5016 cycles.

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 5) {
        result += x[i];
        result += x[i + 1];
        result += x[i + 2];
        result += x[i + 3];
        result += x[i + 4];
    }
    return result;
}
```

4016 cycles. “Are we done yet?”

original. Try unrolling:

```
(int *x)
```

```
result = 0;
```

```
;
```

```
for (i = 0; i < 1000; i += 2) {
```

```
    result += x[i];
```

```
    result += x[i + 1];
```

```
return result;
```

cycles.

10

```
int sum(int *x)
```

```
{
```

```
    int result = 0;
```

```
    int i;
```

```
    for (i = 0; i < 1000; i += 5) {
```

```
        result += x[i];
```

```
        result += x[i + 1];
```

```
        result += x[i + 2];
```

```
        result += x[i + 3];
```

```
        result += x[i + 4];
```

```
    }
```

```
    return result;
```

```
}
```

4016 cycles. “Are we done yet?”

11

“Why is

Didn't w

in makin

Try unrolling:

```
;  
  
1000;i += 2) {  
i];  
i + 1];
```

10

```
int sum(int *x)  
{  
    int result = 0;  
    int i;  
    for (i = 0;i < 1000;i += 5) {  
        result += x[i];  
        result += x[i + 1];  
        result += x[i + 2];  
        result += x[i + 3];  
        result += x[i + 4];  
    }  
    return result;  
}
```

4016 cycles. “Are we done yet?”

11

“Why is this bad p
Didn't we succeed
in making code tw

10

ng:

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 5) {
        result += x[i];
        result += x[i + 1];
        result += x[i + 2];
        result += x[i + 3];
        result += x[i + 4];
    }
    return result;
}
```

= 2) {

4016 cycles. “Are we done yet?”

11

“Why is this bad practice?
Didn't we succeed
in making code twice as fast?”

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 5) {
        result += x[i];
        result += x[i + 1];
        result += x[i + 2];
        result += x[i + 3];
        result += x[i + 4];
    }
    return result;
}
```

4016 cycles. “Are we done yet?”

“Why is this bad practice?
Didn't we succeed
in making code twice as fast?”

```
int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 5) {
        result += x[i];
        result += x[i + 1];
        result += x[i + 2];
        result += x[i + 3];
        result += x[i + 4];
    }
    return result;
}
```

4016 cycles. “Are we done yet?”

“Why is this bad practice?

Didn't we succeed

in making code twice as fast?”

Yes, but CPU time is still

nowhere near optimal,

and human time was wasted.

```

int sum(int *x)
{
    int result = 0;
    int i;
    for (i = 0; i < 1000; i += 5) {
        result += x[i];
        result += x[i + 1];
        result += x[i + 2];
        result += x[i + 3];
        result += x[i + 4];
    }
    return result;
}

```

4016 cycles. “Are we done yet?”

“Why is this bad practice?

Didn't we succeed

in making code twice as fast?”

Yes, but CPU time is still

nowhere near optimal,

and human time was wasted.

Good practice:

Figure out lower bound for

cycles spent on arithmetic etc.

Understand gap between

lower bound and observed time.

```

(int *x)
result = 0;
;
for (i = 0; i < 1000; i += 5) {
    result += x[i];
    result += x[i + 1];
    result += x[i + 2];
    result += x[i + 3];
    result += x[i + 4];
}
return result;

```

cycles. “Are we done yet?”

11

“Why is this bad practice?
 Didn’t we succeed
 in making code twice as fast?”

Yes, but CPU time is still
 nowhere near optimal,
 and human time was wasted.

Good practice:

Figure out lower bound for
 cycles spent on arithmetic etc.

Understand gap between
 lower bound and observed time.

12

Find “A
 Technical
 Rely on
 M4F = I


```

;
1000; i += 5) {
i];
i + 1];
i + 2];
i + 3];
i + 4];

```

we done yet?"

“Why is this bad practice?
Didn't we succeed
in making code twice as fast?”

Yes, but CPU time is still
nowhere near optimal,
and human time was wasted.

Good practice:
Figure out lower bound for
cycles spent on arithmetic etc.
Understand gap between
lower bound and observed time.

Find “ARM Cortex
Technical Referenc
Rely on Wikipedia
 $M4F = M4 + float$

“Why is this bad practice?
 Didn't we succeed
 in making code twice as fast?”

Yes, but CPU time is still
 nowhere near optimal,
 and human time was wasted.

Good practice:

Figure out lower bound for
 cycles spent on arithmetic etc.

Understand gap between
 lower bound and observed time.

= 5) {

yet?”

Find “ARM Cortex-M4 Pro
 Technical Reference Manual
 Rely on Wikipedia comment
 M4F = M4 + floating-point

“Why is this bad practice?
Didn't we succeed
in making code twice as fast?”

Yes, but CPU time is still
nowhere near optimal,
and human time was wasted.

Good practice:
Figure out lower bound for
cycles spent on arithmetic etc.
Understand gap between
lower bound and observed time.

Find “ARM Cortex-M4 Processor
Technical Reference Manual” .
Rely on Wikipedia comment that
 $M4F = M4 + \text{floating-point unit}$.

“Why is this bad practice?
Didn't we succeed
in making code twice as fast?”
Yes, but CPU time is still
nowhere near optimal,
and human time was wasted.
Good practice:
Figure out lower bound for
cycles spent on arithmetic etc.
Understand gap between
lower bound and observed time.

Find “ARM Cortex-M4 Processor
Technical Reference Manual” .
Rely on Wikipedia comment that
 $M4F = M4 + \text{floating-point unit}$.
Manual says that Cortex-M4
“implements the ARMv7E-M
architecture profile” .

“Why is this bad practice?
Didn't we succeed
in making code twice as fast?”

Yes, but CPU time is still
nowhere near optimal,
and human time was wasted.

Good practice:

Figure out lower bound for
cycles spent on arithmetic etc.

Understand gap between
lower bound and observed time.

Find “ARM Cortex-M4 Processor
Technical Reference Manual” .

Rely on Wikipedia comment that
 $M4F = M4 + \text{floating-point unit}$.

Manual says that Cortex-M4
“implements the ARMv7E-M
architecture profile” .

Points to the “ARMv7-M
Architecture Reference Manual” ,
which defines instructions:
e.g., “ADD” for 32-bit addition.

First manual says that
ADD takes just 1 cycle.

this bad practice?

we succeed

ing code twice as fast?"

CPU time is still

near optimal,

man time was wasted.

actice:

ut lower bound for

pent on arithmetic etc.

and gap between

ound and observed time.

12

Find "ARM Cortex-M4 Processor
Technical Reference Manual".

Rely on Wikipedia comment that
 $M4F = M4 + \text{floating-point unit}$.

Manual says that Cortex-M4
"implements the ARMv7E-M
architecture profile".

Points to the "ARMv7-M
Architecture Reference Manual",
which defines instructions:
e.g., "ADD" for 32-bit addition.

First manual says that
ADD takes just 1 cycle.

13

Inputs a

"integer

has 16 in

special-p

and "pro

practice?

vice as fast?"

e is still

mal,

was wasted.

ound for

ithmetic etc.

etween

observed time.

Find "ARM Cortex-M4 Processor Technical Reference Manual".

Rely on Wikipedia comment that $M4F = M4 + \text{floating-point unit}$.

Manual says that Cortex-M4 "implements the ARMv7E-M architecture profile".

Points to the "ARMv7-M Architecture Reference Manual", which defines instructions: e.g., "ADD" for 32-bit addition.

First manual says that ADD takes just 1 cycle.

Inputs and output "integer registers" has 16 integer reg special-purpose "s and "program cou

Find “ARM Cortex-M4 Processor Technical Reference Manual” .

Rely on Wikipedia comment that $M4F = M4 + \text{floating-point unit}$.

Manual says that Cortex-M4 “implements the ARMv7E-M architecture profile” .

Points to the “ARMv7-M Architecture Reference Manual” , which defines instructions: e.g., “ADD” for 32-bit addition.

First manual says that ADD takes just 1 cycle.

Inputs and output of ADD a “integer registers” . ARMv7- has 16 integer registers, incl special-purpose “stack pointer” and “program counter” .

Find “ARM Cortex-M4 Processor Technical Reference Manual” .

Rely on Wikipedia comment that $M4F = M4 + \text{floating-point unit}$.

Manual says that Cortex-M4 “implements the ARMv7E-M architecture profile” .

Points to the “ARMv7-M Architecture Reference Manual” , which defines instructions: e.g., “ADD” for 32-bit addition.

First manual says that ADD takes just 1 cycle.

Inputs and output of ADD are “integer registers” . ARMv7-M has 16 integer registers, including special-purpose “stack pointer” and “program counter” .

Find “ARM Cortex-M4 Processor Technical Reference Manual” .

Rely on Wikipedia comment that $M4F = M4 + \text{floating-point unit}$.

Manual says that Cortex-M4

“implements the ARMv7E-M architecture profile” .

Points to the “ARMv7-M Architecture Reference Manual” , which defines instructions:

e.g., “ADD” for 32-bit addition.

First manual says that

ADD takes just 1 cycle.

Inputs and output of ADD are “integer registers” . ARMv7-M has 16 integer registers, including special-purpose “stack pointer” and “program counter” .

Each element of x array needs to be “loaded” into a register.

Find “ARM Cortex-M4 Processor Technical Reference Manual” .

Rely on Wikipedia comment that $M4F = M4 + \text{floating-point unit}$.

Manual says that Cortex-M4

“implements the ARMv7E-M architecture profile” .

Points to the “ARMv7-M

Architecture Reference Manual” , which defines instructions:

e.g., “ADD” for 32-bit addition.

First manual says that

ADD takes just 1 cycle.

Inputs and output of ADD are “integer registers” . ARMv7-M has 16 integer registers, including special-purpose “stack pointer” and “program counter” .

Each element of x array needs to be “loaded” into a register.

Basic load instruction: LDR.

Manual says 2 cycles but adds a note about “pipelining” .

Then more explanation: if next instruction is also LDR (with address not based on first LDR) then it saves 1 cycle.

ARM Cortex-M4 Processor
Reference Manual”.

Wikipedia comment that
M4 + floating-point unit.

says that Cortex-M4
implements the ARMv7E-M
architecture profile”.

to the “ARMv7-M
Architecture Reference Manual”,
defines instructions:

“ADD” for 32-bit addition.

Manual says that
takes just 1 cycle.

Inputs and output of ADD are
“integer registers”. ARMv7-M
has 16 integer registers, including
special-purpose “stack pointer”
and “program counter”.

Each element of x array needs to
be “loaded” into a register.

Basic load instruction: LDR.

Manual says 2 cycles but adds
a note about “pipelining”.

Then more explanation: if next
instruction is also LDR (with
address not based on first LDR)
then it saves 1 cycle.

n consec
takes on
(“more
pipelined

Can ach
in other
but noth

Lower bo
 $2n + 1$ c
including

Why obs
non-cons
costs of

Cortex-M4 Processor
Reference Manual”.

comment that
the unit.

Cortex-M4

ARMv7E-M

”.

ARMv7-M

Reference Manual”,

instructions:

2-bit addition.

that

cycle.

Inputs and output of ADD are
“integer registers”. ARMv7-M
has 16 integer registers, including
special-purpose “stack pointer”
and “program counter”.

Each element of x array needs to
be “loaded” into a register.

Basic load instruction: LDR.

Manual says 2 cycles but adds
a note about “pipelining”.

Then more explanation: if next
instruction is also LDR (with
address not based on first LDR)
then it saves 1 cycle.

n consecutive LDR
takes only $n + 1$ cycles
(“more multiple LDR
pipelined together”)

Can achieve this savings
in other ways (LDR
but nothing seems

Lower bound for n
 $2n + 1$ cycles,
including n cycles

Why observed time
non-consecutive LDR
costs of manipulating

Inputs and output of ADD are “integer registers”. ARMv7-M has 16 integer registers, including special-purpose “stack pointer” and “program counter”.

Each element of x array needs to be “loaded” into a register.

Basic load instruction: LDR.

Manual says 2 cycles but adds a note about “pipelining”.

Then more explanation: if next instruction is also LDR (with address not based on first LDR) then it saves 1 cycle.

n consecutive LDRs takes only $n + 1$ cycles (“more multiple LDRs can be pipelined together”).

Can achieve this speed in other ways (LDRD, LDM) but nothing seems faster.

Lower bound for n LDR + n $2n + 1$ cycles, including n cycles of arithmetic.

Why observed time is higher for non-consecutive LDRs; costs of manipulating i .

Inputs and output of ADD are “integer registers”. ARMv7-M has 16 integer registers, including special-purpose “stack pointer” and “program counter”.

Each element of x array needs to be “loaded” into a register.

Basic load instruction: LDR.

Manual says 2 cycles but adds a note about “pipelining”.

Then more explanation: if next instruction is also LDR (with address not based on first LDR) then it saves 1 cycle.

n consecutive LDRs takes only $n + 1$ cycles (“more multiple LDRs can be pipelined together”).

Can achieve this speed in other ways (LDRD, LDM) but nothing seems faster.

Lower bound for n LDR + n ADD: $2n + 1$ cycles, including n cycles of arithmetic.

Why observed time is higher: non-consecutive LDRs; costs of manipulating i .

and output of ADD are registers". ARMv7-M integer registers, including purpose "stack pointer" "program counter".

element of x array needs to be "loaded" into a register.

load instruction: LDR.

says 2 cycles but adds about "pipelining".

more explanation: if next instruction is also LDR (with pipeline not based on first LDR) saves 1 cycle.

n consecutive LDRs takes only $n + 1$ cycles ("more multiple LDRs can be pipelined together").

Can achieve this speed in other ways (LDRD, LDM) but nothing seems faster.

Lower bound for n LDR + n ADD: $2n + 1$ cycles, including n cycles of arithmetic.

Why observed time is higher: non-consecutive LDRs; costs of manipulating i .

```
int sum
{
    int r
    int *p
    int x0
        x1
        x2
        x3
        x4
        x5
        x6
```


of ADD are
 . ARMv7-M
 isters, including
 tack pointer”
 nter”.

array needs to
 a register.

tion: LDR.

les but adds
 elining”.

ation: if next

LDR (with
 on first LDR)
 cle.

n consecutive LDRs
 takes only $n + 1$ cycles
 (“more multiple LDRs can be
 pipelined together”).

Can achieve this speed
 in other ways (LDRD, LDM)
 but nothing seems faster.

Lower bound for n LDR + n ADD:
 $2n + 1$ cycles,
 including n cycles of arithmetic.

Why observed time is higher:
 non-consecutive LDRs;
 costs of manipulating i .

```
int sum(int *x)
{
    int result = 0
    int *y = x + 1
    int x0,x1,x2,x
        x5,x6,x7,x

    while (x != y)
        x0 = 0[(vola
        x1 = 1[(vola
        x2 = 2[(vola
        x3 = 3[(vola
        x4 = 4[(vola
        x5 = 5[(vola
        x6 = 6[(vola
```

n consecutive LDRs
takes only $n + 1$ cycles
(“more multiple LDRs can be
pipelined together”).

Can achieve this speed
in other ways (LDRD, LDM)
but nothing seems faster.

Lower bound for n LDR + n ADD:
 $2n + 1$ cycles,
including n cycles of arithmetic.

Why observed time is higher:
non-consecutive LDRs;
costs of manipulating i .

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    int x0,x1,x2,x3,x4,
        x5,x6,x7,x8,x9;

    while (x != y) {
        x0 = 0[(volatile int
        x1 = 1[(volatile int
        x2 = 2[(volatile int
        x3 = 3[(volatile int
        x4 = 4[(volatile int
        x5 = 5[(volatile int
        x6 = 6[(volatile int
```

n consecutive LDRs
 takes only $n + 1$ cycles
 (“more multiple LDRs can be
 pipelined together”).

Can achieve this speed
 in other ways (LDRD, LDM)
 but nothing seems faster.

Lower bound for n LDR + n ADD:
 $2n + 1$ cycles,
 including n cycles of arithmetic.

Why observed time is higher:
 non-consecutive LDRs;
 costs of manipulating i .

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    int x0,x1,x2,x3,x4,
        x5,x6,x7,x8,x9;

    while (x != y) {
        x0 = 0[(volatile int *)x];
        x1 = 1[(volatile int *)x];
        x2 = 2[(volatile int *)x];
        x3 = 3[(volatile int *)x];
        x4 = 4[(volatile int *)x];
        x5 = 5[(volatile int *)x];
        x6 = 6[(volatile int *)x];
```

secutive LDRs

ly $n + 1$ cycles

multiple LDRs can be
d together").

ieve this speed

ways (LDRD, LDM)

ing seems faster.

ound for n LDR + n ADD:

cycles,

g n cycles of arithmetic.

erved time is higher:

secutive LDRs;

manipulating i .

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    int x0,x1,x2,x3,x4,
        x5,x6,x7,x8,x9;
```

```
while (x != y) {
```

```
    x0 = 0[(volatile int *)x];
```

```
    x1 = 1[(volatile int *)x];
```

```
    x2 = 2[(volatile int *)x];
```

```
    x3 = 3[(volatile int *)x];
```

```
    x4 = 4[(volatile int *)x];
```

```
    x5 = 5[(volatile int *)x];
```

```
    x6 = 6[(volatile int *)x];
```

x7 =

x8 =

x9 =

resu

resu

resu

resu

resu

resu

resu

resu

resu

resu

x0 =

x1 =

15

```

int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    int x0,x1,x2,x3,x4,
        x5,x6,x7,x8,x9;

    while (x != y) {
        x0 = 0[(volatile int *)x];
        x1 = 1[(volatile int *)x];
        x2 = 2[(volatile int *)x];
        x3 = 3[(volatile int *)x];
        x4 = 4[(volatile int *)x];
        x5 = 5[(volatile int *)x];
        x6 = 6[(volatile int *)x];

```

16

```

x7 = 7[(volatile int *)x];
x8 = 8[(volatile int *)x];
x9 = 9[(volatile int *)x];
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;
result += x6;
result += x7;
result += x8;
result += x9;
x0 = 10[(volatile int *)x];
x1 = 11[(volatile int *)x];

```

15

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    int x0,x1,x2,x3,x4,
        x5,x6,x7,x8,x9;

    while (x != y) {
        x0 = 0[(volatile int *)x];
        x1 = 1[(volatile int *)x];
        x2 = 2[(volatile int *)x];
        x3 = 3[(volatile int *)x];
        x4 = 4[(volatile int *)x];
        x5 = 5[(volatile int *)x];
        x6 = 6[(volatile int *)x];
```

16

```
x7 = 7[(volatile int *)x];
x8 = 8[(volatile int *)x];
x9 = 9[(volatile int *)x];
    result += x0;
    result += x1;
    result += x2;
    result += x3;
    result += x4;
    result += x5;
    result += x6;
    result += x7;
    result += x8;
    result += x9;
    x0 = 10[(volatile int *)x];
    x1 = 11[(volatile int *)x];
```

```
int sum(int *x)
{
    int result = 0;
    int *y = x + 1000;
    int x0,x1,x2,x3,x4,
        x5,x6,x7,x8,x9;

    while (x != y) {
        x0 = 0[(volatile int *)x];
        x1 = 1[(volatile int *)x];
        x2 = 2[(volatile int *)x];
        x3 = 3[(volatile int *)x];
        x4 = 4[(volatile int *)x];
        x5 = 5[(volatile int *)x];
        x6 = 6[(volatile int *)x];
```

```
x7 = 7[(volatile int *)x];
x8 = 8[(volatile int *)x];
x9 = 9[(volatile int *)x];
    result += x0;
    result += x1;
    result += x2;
    result += x3;
    result += x4;
    result += x5;
    result += x6;
    result += x7;
    result += x8;
    result += x9;
    x0 = 10[(volatile int *)x];
    x1 = 11[(volatile int *)x];
```

```

(int *x)

result = 0;
y = x + 1000;
0, x1, x2, x3, x4,
5, x6, x7, x8, x9;

(x != y) {
= 0[(volatile int *)x];
= 1[(volatile int *)x];
= 2[(volatile int *)x];
= 3[(volatile int *)x];
= 4[(volatile int *)x];
= 5[(volatile int *)x];
= 6[(volatile int *)x];

```

```

x7 = 7[(volatile int *)x];
x8 = 8[(volatile int *)x];
x9 = 9[(volatile int *)x];
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;
result += x6;
result += x7;
result += x8;
result += x9;
x0 = 10[(volatile int *)x];
x1 = 11[(volatile int *)x];

```

```

x2 =
x3 =
x4 =
x5 =
x6 =
x7 =
x8 =
x9 =
x +=
result
result
result
result
result

```


16

```

;
000;
3,x4,
8,x9;

{
tile int *)x];
tile int *)x];
tile int *)x];
tile int *)x];
tile int *)x];
tile int *)x];
tile int *)x];

```

```

x7 = 7[(volatile int *)x];
x8 = 8[(volatile int *)x];
x9 = 9[(volatile int *)x];
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;
result += x6;
result += x7;
result += x8;
result += x9;
x0 = 10[(volatile int *)x];
x1 = 11[(volatile int *)x];

```

17

```

x2 = 12[(vol
x3 = 13[(vol
x4 = 14[(vol
x5 = 15[(vol
x6 = 16[(vol
x7 = 17[(vol
x8 = 18[(vol
x9 = 19[(vol
x += 20;
result += x0
result += x1
result += x2
result += x3
result += x4
result += x5

```

```
x7 = 7[(volatile int *)x];
x8 = 8[(volatile int *)x];
x9 = 9[(volatile int *)x];
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;
result += x6;
result += x7;
result += x8;
result += x9;
x0 = 10[(volatile int *)x];
x1 = 11[(volatile int *)x];
```

```
x2 = 12[(volatile int *)x];
x3 = 13[(volatile int *)x];
x4 = 14[(volatile int *)x];
x5 = 15[(volatile int *)x];
x6 = 16[(volatile int *)x];
x7 = 17[(volatile int *)x];
x8 = 18[(volatile int *)x];
x9 = 19[(volatile int *)x];
x += 20;
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;
```

```
x7 = 7[(volatile int *)x];
x8 = 8[(volatile int *)x];
x9 = 9[(volatile int *)x];
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;
result += x6;
result += x7;
result += x8;
result += x9;
x0 = 10[(volatile int *)x];
x1 = 11[(volatile int *)x];
```

```
x2 = 12[(volatile int *)x];
x3 = 13[(volatile int *)x];
x4 = 14[(volatile int *)x];
x5 = 15[(volatile int *)x];
x6 = 16[(volatile int *)x];
x7 = 17[(volatile int *)x];
x8 = 18[(volatile int *)x];
x9 = 19[(volatile int *)x];
x += 20;
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;
```

```
= 7[(volatile int *)x];
= 8[(volatile int *)x];
= 9[(volatile int *)x];

ult += x0;
ult += x1;
ult += x2;
ult += x3;
ult += x4;
ult += x5;
ult += x6;
ult += x7;
ult += x8;
ult += x9;

= 10[(volatile int *)x];
= 11[(volatile int *)x];
```

```
x2 = 12[(volatile int *)x];
x3 = 13[(volatile int *)x];
x4 = 14[(volatile int *)x];
x5 = 15[(volatile int *)x];
x6 = 16[(volatile int *)x];
x7 = 17[(volatile int *)x];
x8 = 18[(volatile int *)x];
x9 = 19[(volatile int *)x];

x += 20;

result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;

result
result
result
result
}
return
}
```

```
17  
volatile int *)x];  
volatile int *)x];  
volatile int *)x];  
;  
;  
;  
;  
;  
;  
;  
;  
;  
;  
volatile int *)x];  
volatile int *)x];
```

```
18  
x2 = 12[(volatile int *)x];  
x3 = 13[(volatile int *)x];  
x4 = 14[(volatile int *)x];  
x5 = 15[(volatile int *)x];  
x6 = 16[(volatile int *)x];  
x7 = 17[(volatile int *)x];  
x8 = 18[(volatile int *)x];  
x9 = 19[(volatile int *)x];  
x += 20;  
result += x0;  
result += x1;  
result += x2;  
result += x3;  
result += x4;  
result += x5;
```

```
result += x6  
result += x7  
result += x8  
result += x9  
}  
return result;  
}
```

17

```
*)x];  
*)x];  
*)x];
```

```
x2 = 12[(volatile int *)x];  
x3 = 13[(volatile int *)x];  
x4 = 14[(volatile int *)x];  
x5 = 15[(volatile int *)x];  
x6 = 16[(volatile int *)x];  
x7 = 17[(volatile int *)x];  
x8 = 18[(volatile int *)x];  
x9 = 19[(volatile int *)x];  
  
x += 20;  
  
result += x0;  
result += x1;  
result += x2;  
result += x3;  
  
*)x];  
*)x];  
result += x4;  
result += x5;
```

18

```
result += x6;  
result += x7;  
result += x8;  
result += x9;  
}  
  
return result;  
}
```

```
x2 = 12[(volatile int *)x];
x3 = 13[(volatile int *)x];
x4 = 14[(volatile int *)x];
x5 = 15[(volatile int *)x];
x6 = 16[(volatile int *)x];
x7 = 17[(volatile int *)x];
x8 = 18[(volatile int *)x];
x9 = 19[(volatile int *)x];
x += 20;
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;
```

```
    result += x6;
    result += x7;
    result += x8;
    result += x9;
}

return result;
}
```

```
x2 = 12[(volatile int *)x];
x3 = 13[(volatile int *)x];
x4 = 14[(volatile int *)x];
x5 = 15[(volatile int *)x];
x6 = 16[(volatile int *)x];
x7 = 17[(volatile int *)x];
x8 = 18[(volatile int *)x];
x9 = 19[(volatile int *)x];
x += 20;
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;
```

```
    result += x6;
    result += x7;
    result += x8;
    result += x9;
}

return result;
}
```

2526 cycles. Even better in asm.


```

x2 = 12[(volatile int *)x];
x3 = 13[(volatile int *)x];
x4 = 14[(volatile int *)x];
x5 = 15[(volatile int *)x];
x6 = 16[(volatile int *)x];
x7 = 17[(volatile int *)x];
x8 = 18[(volatile int *)x];
x9 = 19[(volatile int *)x];
x += 20;
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;

```

```

    result += x6;
    result += x7;
    result += x8;
    result += x9;
}

return result;
}

```

2526 cycles. Even better in asm.

Wikipedia: “By the late 1990s for even performance sensitive code, optimizing compilers exceeded the performance of human experts.”

```
x2 = 12[(volatile int *)x];
x3 = 13[(volatile int *)x];
x4 = 14[(volatile int *)x];
x5 = 15[(volatile int *)x];
x6 = 16[(volatile int *)x];
x7 = 17[(volatile int *)x];
x8 = 18[(volatile int *)x];
x9 = 19[(volatile int *)x];
x += 20;
result += x0;
result += x1;
result += x2;
result += x3;
result += x4;
result += x5;
```

```
    result += x6;
    result += x7;
    result += x8;
    result += x9;
}

return result;
}
```

2526 cycles. Even better in asm.

Wikipedia: “By the late 1990s for even performance sensitive code, optimizing compilers exceeded the performance of human experts.”

— [citation needed]

```
18
= 12[(volatile int *)x];
= 13[(volatile int *)x];
= 14[(volatile int *)x];
= 15[(volatile int *)x];
= 16[(volatile int *)x];
= 17[(volatile int *)x];
= 18[(volatile int *)x];
= 19[(volatile int *)x];
= 20;

ult += x0;
ult += x1;
ult += x2;
ult += x3;
ult += x4;
ult += x5;
```

```
19
    result += x6;
    result += x7;
    result += x8;
    result += x9;
}

return result;
}
```

2526 cycles. Even better in asm.

Wikipedia: “By the late 1990s for even performance sensitive code, optimizing compilers exceeded the performance of human experts.”

— [citation needed]

19
A real ex
Salsa20
30.25 cy
Lower bo
64 bytes
21 · 16 1
20 · 16 1
so at lea

Also ma
ARMv7-
includes
as part o
(Compile


```

    result += x6;
    result += x7;
    result += x8;
    result += x9;
}

return result;
}

```

2526 cycles. Even better in asm.

Wikipedia: “By the late 1990s for even performance sensitive code, optimizing compilers exceeded the performance of human experts.”

— [citation needed]

A real example

Salsa20 reference software:
30.25 cycles/byte on this CPU.

Lower bound for arithmetic:

64 bytes require

21 · 16 1-cycle ADDs,

20 · 16 1-cycle XORs,

so at least 10.25 cycles/byte.

Also many rotations, but

ARMv7-M instruction set

includes free rotation

as part of XOR instruction.

(Compiler knows this.)

```

ult += x6;
ult += x7;
ult += x8;
ult += x9;

```

```

n result;

```

cles. Even better in asm.

ia: “By the late 1990s for
performance sensitive code,
ng compilers exceeded the
ance of human experts.”

tion needed]

A real example

Salsa20 reference software:
30.25 cycles/byte on this CPU.

Lower bound for arithmetic:

64 bytes require

21 · 16 1-cycle ADDs,

20 · 16 1-cycle XORs,

so at least 10.25 cycles/byte.

Also many rotations, but

ARMv7-M instruction set

includes free rotation

as part of XOR instruction.

(Compiler knows this.)

Detailed

several c

load_li

store_l

Can repl

(Compile

Then ob

18 cycles

plus 5 cy

Still far

A real example

Salsa20 reference software:
30.25 cycles/byte on this CPU.

Lower bound for arithmetic:
64 bytes require
21 · 16 1-cycle ADDs,
20 · 16 1-cycle XORs,
so at least 10.25 cycles/byte.

Also many rotations, but
ARMv7-M instruction set
includes free rotation
as part of XOR instruction.
(Compiler knows this.)

Detailed benchmark
several cycles/byte
load_littleendian
store_littleendian

Can replace with LDR
(Compiler doesn't

Then observe 23 cycles/byte
18 cycles/byte for
plus 5 cycles/byte
Still far above 10.25

better in asm.

the late 1990s for
sensitive code,
ers exceeded the
man experts.”

d]

A real example

Salsa20 reference software:
30.25 cycles/byte on this CPU.

Lower bound for arithmetic:
64 bytes require
21 · 16 1-cycle ADDs,
20 · 16 1-cycle XORs,
so at least 10.25 cycles/byte.

Also many rotations, but
ARMv7-M instruction set
includes free rotation
as part of XOR instruction.
(Compiler knows this.)

asm.
00s for
code,
ed the
rts.”

Detailed benchmarks show
several cycles/byte spent on
load_littleendian and
store_littleendian.

Can replace with LDR and STR
(Compiler doesn't see this.)

Then observe 23 cycles/byte
18 cycles/byte for rounds,
plus 5 cycles/byte overhead.
Still far above 10.25 cycles/

A real example

Salsa20 reference software:
30.25 cycles/byte on this CPU.

Lower bound for arithmetic:

64 bytes require

21 · 16 1-cycle ADDs,

20 · 16 1-cycle XORs,

so at least 10.25 cycles/byte.

Also many rotations, but
ARMv7-M instruction set
includes free rotation
as part of XOR instruction.
(Compiler knows this.)

Detailed benchmarks show
several cycles/byte spent on
load_littleendian and
store_littleendian.

Can replace with LDR and STR.
(Compiler doesn't see this.)

Then observe 23 cycles/byte:
18 cycles/byte for rounds,
plus 5 cycles/byte overhead.
Still far above 10.25 cycles/byte.

A real example

Salsa20 reference software:
30.25 cycles/byte on this CPU.

Lower bound for arithmetic:

64 bytes require

21 · 16 1-cycle ADDs,

20 · 16 1-cycle XORs,

so at least 10.25 cycles/byte.

Also many rotations, but
ARMv7-M instruction set
includes free rotation
as part of XOR instruction.
(Compiler knows this.)

Detailed benchmarks show
several cycles/byte spent on
load_littleendian and
store_littleendian.

Can replace with LDR and STR.
(Compiler doesn't see this.)

Then observe 23 cycles/byte:
18 cycles/byte for rounds,
plus 5 cycles/byte overhead.
Still far above 10.25 cycles/byte.

Gap is mostly loads, stores.
Minimize load/store cost by
choosing “spills” carefully.

Example

reference software:

cycles/byte on this CPU.

bound for arithmetic:

require

1-cycle ADDs,

1-cycle XORs,

at least 10.25 cycles/byte.

any rotations, but

ARM instruction set

free rotation

of XOR instruction.

(Everyone knows this.)

Detailed benchmarks show
several cycles/byte spent on
`load_littleendian` and
`store_littleendian`.

Can replace with `LDR` and `STR`.
(Compiler doesn't see this.)

Then observe 23 cycles/byte:
18 cycles/byte for rounds,
plus 5 cycles/byte overhead.
Still far above 10.25 cycles/byte.

Gap is mostly loads, stores.
Minimize load/store cost by
choosing "spills" carefully.

Which o
should b
Don't tr
optimize

software:
on this CPU.

arithmetic:

Ds,

Rs,

cycles/byte.

ns, but

tion set

ion

struction.

this.)

Detailed benchmarks show
several cycles/byte spent on
`load_littleendian` and
`store_littleendian`.

Can replace with LDR and STR.
(Compiler doesn't see this.)

Then observe 23 cycles/byte:
18 cycles/byte for rounds,
plus 5 cycles/byte overhead.
Still far above 10.25 cycles/byte.

Gap is mostly loads, stores.
Minimize load/store cost by
choosing "spills" carefully.

Which of the 16 S
should be in regist
Don't trust compi
optimize register a

Detailed benchmarks show several cycles/byte spent on `load_littleendian` and `store_littleendian`.

Can replace with `LDR` and `STR`.
(Compiler doesn't see this.)

Then observe 23 cycles/byte:
18 cycles/byte for rounds,
plus 5 cycles/byte overhead.
Still far above 10.25 cycles/byte.

Gap is mostly loads, stores.
Minimize load/store cost by
choosing "spills" carefully.

Which of the 16 Salsa20 words should be in registers?
Don't trust compiler to optimize register allocation.

Detailed benchmarks show several cycles/byte spent on `load_littleendian` and `store_littleendian`.

Can replace with `LDR` and `STR`.
(Compiler doesn't see this.)

Then observe 23 cycles/byte:
18 cycles/byte for rounds,
plus 5 cycles/byte overhead.
Still far above 10.25 cycles/byte.

Gap is mostly loads, stores.
Minimize load/store cost by
choosing "spills" carefully.

Which of the 16 Salsa20 words
should be in registers?
Don't trust compiler to
optimize register allocation.

Detailed benchmarks show several cycles/byte spent on `load_littleendian` and `store_littleendian`.

Can replace with `LDR` and `STR`.
(Compiler doesn't see this.)

Then observe 23 cycles/byte:
18 cycles/byte for rounds,
plus 5 cycles/byte overhead.
Still far above 10.25 cycles/byte.

Gap is mostly loads, stores.
Minimize load/store cost by
choosing "spills" carefully.

Which of the 16 Salsa20 words should be in registers?

Don't trust compiler to optimize register allocation.

Make loads consecutive?

Don't trust compiler to optimize instruction scheduling.

Detailed benchmarks show several cycles/byte spent on `load_littleendian` and `store_littleendian`.

Can replace with `LDR` and `STR`.
(Compiler doesn't see this.)

Then observe 23 cycles/byte:
18 cycles/byte for rounds,
plus 5 cycles/byte overhead.
Still far above 10.25 cycles/byte.

Gap is mostly loads, stores.
Minimize load/store cost by
choosing "spills" carefully.

Which of the 16 Salsa20 words should be in registers?

Don't trust compiler to optimize register allocation.

Make loads consecutive?

Don't trust compiler to optimize instruction scheduling.

Spill to FPU instead of stack?

Don't trust compiler to optimize instruction selection.

Detailed benchmarks show several cycles/byte spent on `load_littleendian` and `store_littleendian`.

Can replace with `LDR` and `STR`.
(Compiler doesn't see this.)

Then observe 23 cycles/byte:
18 cycles/byte for rounds,
plus 5 cycles/byte overhead.
Still far above 10.25 cycles/byte.

Gap is mostly loads, stores.
Minimize load/store cost by
choosing "spills" carefully.

Which of the 16 Salsa20 words should be in registers?

Don't trust compiler to optimize register allocation.

Make loads consecutive?

Don't trust compiler to optimize instruction scheduling.

Spill to FPU instead of stack?

Don't trust compiler to optimize instruction selection.

On bigger CPUs,
selecting vector instructions
is critical for performance.

benchmarks show
cycles/byte spent on
littleendian and
littleendian.

ace with LDR and STR.
(compiler doesn't see this.)

serve 23 cycles/byte:

s/byte for rounds,
cycles/byte overhead.

above 10.25 cycles/byte.

mostly loads, stores.

the load/store cost by

g "spills" carefully.

Which of the 16 Salsa20 words
should be in registers?

Don't trust compiler to
optimize register allocation.

Make loads consecutive?

Don't trust compiler to
optimize instruction scheduling.

Spill to FPU instead of stack?

Don't trust compiler to
optimize instruction selection.

On bigger CPUs,
selecting vector instructions
is critical for performance.

<https://>

includes

of 614 c

>20 imp

Haswell:

impleme

gcc -O3

is $6.15 \times$

Salsa20

arks show
e spent on
n and
an.

LDR and STR.
(see this.)

cycles/byte:
rounds,
overhead.

25 cycles/byte.

ls, stores.
re cost by
carefully.

Which of the 16 Salsa20 words
should be in registers?

Don't trust compiler to
optimize register allocation.

Make loads consecutive?

Don't trust compiler to
optimize instruction scheduling.

Spill to FPU instead of stack?

Don't trust compiler to
optimize instruction selection.

On bigger CPUs,
selecting vector instructions
is critical for performance.

<https://bench.c>
includes 2392 imp
of 614 cryptograph
>20 implementati

Haswell: Reasonab
implementation co
gcc -O3 -fomit-
is $6.15\times$ slower th
Salsa20 implement

Which of the 16 Salsa20 words should be in registers?

Don't trust compiler to optimize register allocation.

Make loads consecutive?

Don't trust compiler to optimize instruction scheduling.

Spill to FPU instead of stack?

Don't trust compiler to optimize instruction selection.

On bigger CPUs, selecting vector instructions is critical for performance.

<https://bench.cr.yp.to> includes 2392 implementations of 614 cryptographic primitives >20 implementations of Salsa20

Haswell: Reasonably simple implementation compiled with `gcc -O3 -fomit-frame-pointer` is $6.15\times$ slower than fastest Salsa20 implementation.

Which of the 16 Salsa20 words should be in registers?

Don't trust compiler to optimize register allocation.

Make loads consecutive?

Don't trust compiler to optimize instruction scheduling.

Spill to FPU instead of stack?

Don't trust compiler to optimize instruction selection.

On bigger CPUs, selecting vector instructions is critical for performance.

<https://bench.cr.yp.to>

includes 2392 implementations of 614 cryptographic primitives.
>20 implementations of Salsa20.

Haswell: Reasonably simple ref implementation compiled with `gcc -O3 -fomit-frame-pointer` is $6.15\times$ slower than fastest Salsa20 implementation.

Which of the 16 Salsa20 words should be in registers?

Don't trust compiler to optimize register allocation.

Make loads consecutive?

Don't trust compiler to optimize instruction scheduling.

Spill to FPU instead of stack?

Don't trust compiler to optimize instruction selection.

On bigger CPUs, selecting vector instructions is critical for performance.

<https://bench.cr.yp.to>

includes 2392 implementations of 614 cryptographic primitives.
>20 implementations of Salsa20.

Haswell: Reasonably simple ref implementation compiled with `gcc -O3 -fomit-frame-pointer` is $6.15\times$ slower than fastest Salsa20 implementation.

merged implementation with "machine-independent" optimizations and best of 121 compiler options: $4.52\times$ slower.

of the 16 Salsa20 words
 be in registers?
 must compiler to
 e register allocation.

ads consecutive?
 must compiler to
 e instruction scheduling.

FPU instead of stack?
 must compiler to
 e instruction selection.

er CPUs,
 g vector instructions
 l for performance.

<https://bench.cr.yp.to>

includes 2392 implementations
 of 614 cryptographic primitives.
 >20 implementations of Salsa20.

Haswell: Reasonably simple ref
 implementation compiled with
`gcc -O3 -fomit-frame-pointer`
 is $6.15\times$ slower than fastest
 Salsa20 implementation.

merged implementation
 with “machine-independent”
 optimizations and best of 121
 compiler options: $4.52\times$ slower.

Fast ran

Goal: P
 into a ra

<https://bench.cr.yp.to>

includes 2392 implementations
of 614 cryptographic primitives.
>20 implementations of Salsa20.

Haswell: Reasonably simple ref
implementation compiled with
`gcc -O3 -fomit-frame-pointer`
is $6.15\times$ slower than fastest
Salsa20 implementation.

merged implementation
with “machine-independent”
optimizations and best of 121
compiler options: $4.52\times$ slower.

Fast random perm

Goal: Put list $(x_1,$
into a random ord

<https://bench.cr.yp.to>

includes 2392 implementations
of 614 cryptographic primitives.
>20 implementations of Salsa20.

Haswell: Reasonably simple ref
implementation compiled with
`gcc -O3 -fomit-frame-pointer`
is $6.15\times$ slower than fastest
Salsa20 implementation.

merged implementation
with “machine-independent”
optimizations and best of 121
compiler options: $4.52\times$ slower.

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

<https://bench.cr.yp.to>

includes 2392 implementations
of 614 cryptographic primitives.
>20 implementations of Salsa20.

Haswell: Reasonably simple ref
implementation compiled with
`gcc -O3 -fomit-frame-pointer`
is $6.15\times$ slower than fastest
Salsa20 implementation.

merged implementation
with “machine-independent”
optimizations and best of 121
compiler options: $4.52\times$ slower.

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

<https://bench.cr.yp.to>

includes 2392 implementations
of 614 cryptographic primitives.
>20 implementations of Salsa20.

Haswell: Reasonably simple ref
implementation compiled with
`gcc -O3 -fomit-frame-pointer`
is $6.15\times$ slower than fastest
Salsa20 implementation.

merged implementation
with “machine-independent”
optimizations and best of 121
compiler options: $4.52\times$ slower.

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

One textbook strategy:

Sort $(Mr_1 + x_1, \dots, Mr_n + x_n)$ for
random (r_1, \dots, r_n) , suitable M .

<https://bench.cr.yp.to>

includes 2392 implementations
of 614 cryptographic primitives.
>20 implementations of Salsa20.

Haswell: Reasonably simple ref
implementation compiled with
`gcc -O3 -fomit-frame-pointer`
is $6.15\times$ slower than fastest
Salsa20 implementation.

merged implementation
with “machine-independent”
optimizations and best of 121
compiler options: $4.52\times$ slower.

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

One textbook strategy:

Sort $(Mr_1 + x_1, \dots, Mr_n + x_n)$ for
random (r_1, \dots, r_n) , suitable M .

McEliece encryption example:

Randomly order 6960 bits

$(1, \dots, 1, 0, \dots, 0)$, weight 119.

<https://bench.cr.yp.to>

includes 2392 implementations
of 614 cryptographic primitives.
>20 implementations of Salsa20.

Haswell: Reasonably simple ref
implementation compiled with
`gcc -O3 -fomit-frame-pointer`
is $6.15\times$ slower than fastest
Salsa20 implementation.

merged implementation
with “machine-independent”
optimizations and best of 121
compiler options: $4.52\times$ slower.

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

One textbook strategy:

Sort $(Mr_1 + x_1, \dots, Mr_n + x_n)$ for
random (r_1, \dots, r_n) , suitable M .

McEliece encryption example:

Randomly order 6960 bits
 $(1, \dots, 1, 0, \dots, 0)$, weight 119.

NTRU encryption example:

Randomly order 761 trits
 $(\pm 1, \dots, \pm 1, 0, \dots, 0)$, wt 286.

[/bench.cr.yp.to](#)

2392 implementations
cryptographic primitives.
implementations of Salsa20.

Reasonably simple ref
ntation compiled with
-fomit-frame-pointer
slower than fastest
implementation.

implementation
"machine-independent"

tions and best of 121

r options: $4.52\times$ slower.

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

One textbook strategy:

Sort $(Mr_1 + x_1, \dots, Mr_n + x_n)$ for
random (r_1, \dots, r_n) , suitable M .

McEliece encryption example:

Randomly order 6960 bits

$(1, \dots, 1, 0, \dots, 0)$, weight 119.

NTRU encryption example:

Randomly order 761 trits

$(\pm 1, \dots, \pm 1, 0, \dots, 0)$, wt 286.

Simulate
using RM

cr.yp.to

Implementations

basic primitives.

versions of Salsa20.

very simple ref

compiled with

frame-pointer

is the fastest

implementation.

implementation

is "independent"

is the best of 121

is 4.52× slower.

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

One textbook strategy:

Sort $(Mr_1 + x_1, \dots, Mr_n + x_n)$ for
random (r_1, \dots, r_n) , suitable M .

McEliece encryption example:

Randomly order 6960 bits

$(1, \dots, 1, 0, \dots, 0)$, weight 119.

NTRU encryption example:

Randomly order 761 trits

$(\pm 1, \dots, \pm 1, 0, \dots, 0)$, wt 286.

Simulate uniform

using RNG: e.g., s

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

One textbook strategy:

Sort $(Mr_1 + x_1, \dots, Mr_n + x_n)$ for
random (r_1, \dots, r_n) , suitable M .

McEliece encryption example:

Randomly order 6960 bits

$(1, \dots, 1, 0, \dots, 0)$, weight 119.

NTRU encryption example:

Randomly order 761 trits

$(\pm 1, \dots, \pm 1, 0, \dots, 0)$, wt 286.

Simulate uniform random r_i
using RNG: e.g., stream cipher

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

One textbook strategy:

Sort $(Mr_1 + x_1, \dots, Mr_n + x_n)$ for
random (r_1, \dots, r_n) , suitable M .

McEliece encryption example:

Randomly order 6960 bits

$(1, \dots, 1, 0, \dots, 0)$, weight 119.

NTRU encryption example:

Randomly order 761 trits

$(\pm 1, \dots, \pm 1, 0, \dots, 0)$, wt 286.

Simulate uniform random r_i
using RNG: e.g., stream cipher.

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

One textbook strategy:

Sort $(Mr_1 + x_1, \dots, Mr_n + x_n)$ for
random (r_1, \dots, r_n) , suitable M .

McEliece encryption example:

Randomly order 6960 bits

$(1, \dots, 1, 0, \dots, 0)$, weight 119.

NTRU encryption example:

Randomly order 761 trits

$(\pm 1, \dots, \pm 1, 0, \dots, 0)$, wt 286.

Simulate uniform random r_i
using RNG: e.g., stream cipher.

How many bits in r_i ? Negligible
collisions? Occasional collisions?

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

One textbook strategy:

Sort $(Mr_1 + x_1, \dots, Mr_n + x_n)$ for
random (r_1, \dots, r_n) , suitable M .

McEliece encryption example:

Randomly order 6960 bits

$(1, \dots, 1, 0, \dots, 0)$, weight 119.

NTRU encryption example:

Randomly order 761 trits

$(\pm 1, \dots, \pm 1, 0, \dots, 0)$, wt 286.

Simulate uniform random r_i
using RNG: e.g., stream cipher.

How many bits in r_i ? Negligible
collisions? Occasional collisions?

Restart on collision?

Uniform distribution; some cost.

Fast random permutations

Goal: Put list (x_1, \dots, x_n)
into a random order.

One textbook strategy:

Sort $(Mr_1 + x_1, \dots, Mr_n + x_n)$ for
random (r_1, \dots, r_n) , suitable M .

McEliece encryption example:

Randomly order 6960 bits

$(1, \dots, 1, 0, \dots, 0)$, weight 119.

NTRU encryption example:

Randomly order 761 trits

$(\pm 1, \dots, \pm 1, 0, \dots, 0)$, wt 286.

Simulate uniform random r_i
using RNG: e.g., stream cipher.

How many bits in r_i ? Negligible
collisions? Occasional collisions?

Restart on collision?

Uniform distribution; some cost.

Example: $n = 6960$ bits;

weight 119; 31-bit r_i ; no restart.

Any output is produced in

$\leq 119!(n - 119)! \binom{2^{31} + n - 1}{n}$ ways;

i.e., $< 1.02 \cdot 2^{31n} / \binom{n}{119}$ ways.

Factor < 1.02 increase in

attacker's chance of winning.

random permutations

output list (x_1, \dots, x_n)

random order.

textbook strategy:

$(r_1 + x_1, \dots, Mr_n + x_n)$ for
 (r_1, \dots, r_n) , suitable M .

the encryption example:

randomly order 6960 bits

$(\dots, 0, \dots, 0)$, weight 119.

the encryption example:

randomly order 761 trits

$(\dots, \pm 1, 0, \dots, 0)$, wt 286.

Simulate uniform random r_i
 using RNG: e.g., stream cipher.

How many bits in r_i ? Negligible
 collisions? Occasional collisions?

Restart on collision?

Uniform distribution; some cost.

Example: $n = 6960$ bits;

weight 119; 31-bit r_i ; no restart.

Any output is produced in
 $\leq 119!(n - 119)! \binom{2^{31} + n - 1}{n}$ ways;
 i.e., $< 1.02 \cdot 2^{31n} / \binom{n}{119}$ ways.

Factor < 1.02 increase in
 attacker's chance of winning.

Which s

Referenc

$n(n - 1)$

Permutations

(x_1, \dots, x_n)

er.

ategy:

$(x_1, \dots, Mr_n + x_n)$ for

(x_1, \dots, x_n) , suitable M .

on example:

960 bits

, weight 119.

example:

61 trits

$(x_1, \dots, 0)$, wt 286.

Simulate uniform random r_i
using RNG: e.g., stream cipher.

How many bits in r_i ? Negligible
collisions? Occasional collisions?

Restart on collision?

Uniform distribution; some cost.

Example: $n = 6960$ bits;

weight 119; 31-bit r_i ; no restart.

Any output is produced in
 $\leq 119!(n - 119)! \binom{2^{31} + n - 1}{n}$ ways;

i.e., $< 1.02 \cdot 2^{31n} / \binom{n}{119}$ ways.

Factor < 1.02 increase in

attacker's chance of winning.

Which sorting algo

Reference bubbles

$n(n - 1)/2$ minima

Simulate uniform random r_i
using RNG: e.g., stream cipher.

How many bits in r_i ? Negligible
collisions? Occasional collisions?

Restart on collision?

Uniform distribution; some cost.

Example: $n = 6960$ bits;
weight 119; 31-bit r_i ; no restart.

Any output is produced in
 $\leq 119!(n - 119)! \binom{2^{31} + n - 1}{n}$ ways;
i.e., $< 1.02 \cdot 2^{31n} / \binom{n}{119}$ ways.

Factor < 1.02 increase in
attacker's chance of winning.

Which sorting algorithm?

Reference bubblesort code d
 $n(n - 1)/2$ minmax operatio

Simulate uniform random r_i
using RNG: e.g., stream cipher.

How many bits in r_i ? Negligible collisions? Occasional collisions?

Restart on collision?

Uniform distribution; some cost.

Example: $n = 6960$ bits;
weight 119; 31-bit r_i ; no restart.

Any output is produced in
 $\leq 119!(n - 119)! \binom{2^{31} + n - 1}{n}$ ways;
i.e., $< 1.02 \cdot 2^{31n} / \binom{n}{119}$ ways.

Factor < 1.02 increase in
attacker's chance of winning.

Which sorting algorithm?

Reference bubblesort code does
 $n(n - 1)/2$ minmax operations.

Simulate uniform random r_i
 using RNG: e.g., stream cipher.
 How many bits in r_i ? Negligible
 collisions? Occasional collisions?
 Restart on collision?
 Uniform distribution; some cost.
 Example: $n = 6960$ bits;
 weight 119; 31-bit r_i ; no restart.
 Any output is produced in
 $\leq 119!(n - 119)! \binom{2^{31} + n - 1}{n}$ ways;
 i.e., $< 1.02 \cdot 2^{31n} / \binom{n}{119}$ ways.
 Factor < 1.02 increase in
 attacker's chance of winning.

Which sorting algorithm?
 Reference bubblesort code does
 $n(n - 1)/2$ minmax operations.
 Many standard algorithms use
 fewer operations: mergesort,
 quicksort, heapsort, radixsort, etc.
 But these algorithms rely on
 secret branches and secret indices.

Simulate uniform random r_i
 using RNG: e.g., stream cipher.
 How many bits in r_i ? Negligible
 collisions? Occasional collisions?
 Restart on collision?
 Uniform distribution; some cost.
 Example: $n = 6960$ bits;
 weight 119; 31-bit r_i ; no restart.
 Any output is produced in
 $\leq 119!(n - 119)! \binom{2^{31} + n - 1}{n}$ ways;
 i.e., $< 1.02 \cdot 2^{31n} / \binom{n}{119}$ ways.
 Factor < 1.02 increase in
 attacker's chance of winning.

Which sorting algorithm?
 Reference bubblesort code does
 $n(n - 1)/2$ minmax operations.
 Many standard algorithms use
 fewer operations: mergesort,
 quicksort, heapsort, radixsort, etc.
 But these algorithms rely on
 secret branches and secret indices.
 Exercise: convert mergesort
 into constant-time mergesort
 using $\Theta(n^2)$ operations.

e uniform random r_i
 NG: e.g., stream cipher.
 ny bits in r_i ? Negligible
 s? Occasional collisions?
 on collision?
 distribution; some cost.
 e: $n = 6960$ bits;
 19; 31-bit r_i ; no restart.
 put is produced in
 $(n - 119)! \binom{2^{31} + n - 1}{n}$ ways;
 $.02 \cdot 2^{31n} / \binom{n}{119}$ ways.
 < 1.02 increase in
 's chance of winning.

Which sorting algorithm?

Reference bubblesort code does $n(n - 1)/2$ minmax operations.

Many standard algorithms use fewer operations: mergesort, quicksort, heapsort, radixsort, etc.

But these algorithms rely on secret branches and secret indices.

Exercise: convert mergesort into constant-time mergesort using $\Theta(n^2)$ operations.

Converting
 constant
 loses only
 cost of c

random r_i

stream cipher.

r_i ? Negligible
collision collisions?

n?

on; some cost.

60 bits;

r_i ; no restart.

duced in
 $(2^{31} + n - 1)$ ways;

$\binom{n}{119}$ ways.

ease in

of winning.

Which sorting algorithm?

Reference bubblesort code does
 $n(n - 1)/2$ minmax operations.

Many standard algorithms use
fewer operations: mergesort,
quicksort, heapsort, radixsort, etc.

But these algorithms rely on
secret branches and secret indices.

Exercise: convert mergesort
into constant-time mergesort
using $\Theta(n^2)$ operations.

Converting bubble
constant-time bub
loses only a consta
cost of constant-ti

Which sorting algorithm?

Reference bubblesort code does $n(n - 1)/2$ minmax operations.

Many standard algorithms use fewer operations: mergesort, quicksort, heapsort, radixsort, etc.

But these algorithms rely on secret branches and secret indices.

Exercise: convert mergesort into constant-time mergesort using $\Theta(n^2)$ operations.

Converting bubblesort into constant-time bubblesort loses only a constant factor: cost of constant-time minma

Which sorting algorithm?

Reference bubblesort code does $n(n - 1)/2$ minmax operations.

Many standard algorithms use fewer operations: mergesort, quicksort, heapsort, radixsort, etc.

But these algorithms rely on secret branches and secret indices.

Exercise: convert mergesort into constant-time mergesort using $\Theta(n^2)$ operations.

Converting bubblesort into constant-time bubblesort loses only a constant factor: cost of constant-time minmax.

Which sorting algorithm?

Reference bubblesort code does $n(n - 1)/2$ minmax operations.

Many standard algorithms use fewer operations: mergesort, quicksort, heapsort, radixsort, etc.

But these algorithms rely on secret branches and secret indices.

Exercise: convert mergesort into constant-time mergesort using $\Theta(n^2)$ operations.

Converting bubblesort into constant-time bubblesort loses only a constant factor: cost of constant-time minmax.

“Sorting network” :
sorting algorithm built as constant sequence of minmax operations (“comparators”).

Which sorting algorithm?

Reference bubblesort code does $n(n - 1)/2$ minmax operations.

Many standard algorithms use fewer operations: mergesort, quicksort, heapsort, radixsort, etc.

But these algorithms rely on secret branches and secret indices.

Exercise: convert mergesort into constant-time mergesort using $\Theta(n^2)$ operations.

Converting bubblesort into constant-time bubblesort loses only a constant factor: cost of constant-time minmax.

“Sorting network” :
sorting algorithm built as constant sequence of minmax operations (“comparators”).

Sorting network on next slide:
Batcher’s merge-exchange sort.
 $\Theta(n(\log n)^2)$ minmax operations;
 $(1/4)(e^2 - e + 4)n - 1$ for $n = 2^e$.

orting algorithm?

ce bubblesort code does
 $n^2/2$ minmax operations.

andard algorithms use
 operations: mergesort,
 t, heapsort, radixsort, etc.

se algorithms rely on
 ranches and secret indices.

: convert mergesort
 constant-time mergesort
 (n^2) operations.

Converting bubblesort into
 constant-time bubblesort
 loses only a constant factor:
 cost of constant-time minmax.

“Sorting network”:
 sorting algorithm built as
 constant sequence of minmax
 operations (“comparators”).

Sorting network on next slide:

Batcher’s merge-exchange sort.

$\Theta(n(\log n)^2)$ minmax operations;
 $(1/4)(e^2 - e + 4)n - 1$ for $n = 2^e$.

```
void sort
{ long i
  t = 1
  while
  for (j
    for
      i
    for
      f
  }
}
```

Algorithm?

Sort code does
x operations.

Algorithms use
mergesort,
t, radixsort, etc.

ms rely on
and secret indices.

mergesort
e mergesort
tions.

Converting bubblesort into
constant-time bubblesort
loses only a constant factor:
cost of constant-time minmax.

“Sorting network”:
sorting algorithm built as
constant sequence of minmax
operations (“comparators”).

Sorting network on next slide:

Batcher’s merge-exchange sort.

$\Theta(n(\log n)^2)$ minmax operations;

$(1/4)(e^2 - e + 4)n - 1$ for $n = 2^e$.

```
void sort(int32
{ long long t,p,
  t = 1; if (n <
  while (t < n-t
  for (p = t;p >
    for (i = 0;i
      if (!(i &
        minmax(x
  for (q = t;q
    for (i = 0
      if (!(i
        minmax
  }
}
```

Converting bubblesort into
constant-time bubblesort
loses only a constant factor:
cost of constant-time minmax.

“Sorting network”:

sorting algorithm built as
constant sequence of minmax
operations (“comparators”).

Sorting network on next slide:

Batcher’s merge-exchange sort.

$\Theta(n(\log n)^2)$ minmax operations;

$(1/4)(e^2 - e + 4)n - 1$ for $n = 2^e$.

```
void sort(int32 *x, long l
{ long long t,p,q,i;
  t = 1; if (n < 2) retur
  while (t < n-t) t += t;
  for (p = t;p > 0;p >>=
    for (i = 0;i < n-p;++
      if (!(i & p))
        minmax(x+i,x+i+p)
  for (q = t;q > p;q >>
    for (i = 0;i < n-q;
      if (!(i & p))
        minmax(x+i+p,x+
    }
  }
}
```

Converting bubblesort into
constant-time bubblesort
loses only a constant factor:
cost of constant-time minmax.

“Sorting network”:
sorting algorithm built as
constant sequence of minmax
operations (“comparators”).

Sorting network on next slide:
Batcher’s merge-exchange sort.

$\Theta(n(\log n)^2)$ minmax operations;
 $(1/4)(e^2 - e + 4)n - 1$ for $n = 2^e$.

```
void sort(int32 *x, long long n)
{ long long t,p,q,i;
  t = 1; if (n < 2) return;
  while (t < n-t) t += t;
  for (p = t;p > 0;p >>= 1) {
    for (i = 0;i < n-p;++i)
      if (!(i & p))
        minmax(x+i,x+i+p);
    for (q = t;q > p;q >>= 1)
      for (i = 0;i < n-q;++i)
        if (!(i & p))
          minmax(x+i+p,x+i+q);
  }
}
```

ng bubblesort into

constant-time bubblesort

by a constant factor:

constant-time minmax.

network”:

algorithm built as

a sequence of minmax

operations (“comparators”).

network on next slide:

is merge-exchange sort.

(n^2) minmax operations;

$(n^2 - e + 4)n - 1$ for $n = 2^e$.

```
void sort(int32 *x, long long n)
{ long long t,p,q,i;
  t = 1; if (n < 2) return;
  while (t < n-t) t += t;
  for (p = t;p > 0;p >>= 1) {
    for (i = 0;i < n-p;++i)
      if (!(i & p))
        minmax(x+i,x+i+p);
    for (q = t;q > p;q >>= 1)
      for (i = 0;i < n-q;++i)
        if (!(i & p))
          minmax(x+i+p,x+i+q);
  }
}
```

How ma

Intel Has

Every cy

“min” o

8 32-bit

sort into
 blesort
 ant factor:
 me minmax.
 :
 built as
 of minmax
 operators").
 n next slide:
 xchange sort.
 max operations;
 $n - 1$ for $n = 2^e$.

```

void sort(int32 *x, long long n)
{ long long t,p,q,i;
  t = 1; if (n < 2) return;
  while (t < n-t) t += t;
  for (p = t;p > 0;p >>= 1) {
    for (i = 0;i < n-p;++i)
      if (!(i & p))
        minmax(x+i,x+i+p);
    for (q = t;q > p;q >>= 1)
      for (i = 0;i < n-q;++i)
        if (!(i & p))
          minmax(x+i+p,x+i+q);
  }
}

```

How many cycles
 Intel Haswell CPU
 Every cycle: a vec
 "min" operations
 8 32-bit "max" op

```

void sort(int32 *x, long long n)
{ long long t,p,q,i;
  t = 1; if (n < 2) return;
  while (t < n-t) t += t;
  for (p = t;p > 0;p >>= 1) {
    for (i = 0;i < n-p;++i)
      if (!(i & p))
        minmax(x+i,x+i+p);
    for (q = t;q > p;q >>= 1)
      for (i = 0;i < n-q;++i)
        if (!(i & p))
          minmax(x+i+p,x+i+q);
  }
}

```

How many cycles on, e.g.,
Intel Haswell CPU core?

Every cycle: a vector of 8 32-bit
“min” operations and a vector
of 8 32-bit “max” operations.


```

void sort(int32 *x, long long n)
{ long long t,p,q,i;
  t = 1; if (n < 2) return;
  while (t < n-t) t += t;
  for (p = t;p > 0;p >>= 1) {
    for (i = 0;i < n-p;++i)
      if (!(i & p))
        minmax(x+i,x+i+p);
    for (q = t;q > p;q >>= 1)
      for (i = 0;i < n-q;++i)
        if (!(i & p))
          minmax(x+i+p,x+i+q);
  }
}

```

How many cycles on, e.g.,
Intel Haswell CPU core?

Every cycle: a vector of 8 32-bit
“min” operations and a vector of
8 32-bit “max” operations.

```

void sort(int32 *x, long long n)
{ long long t,p,q,i;
  t = 1; if (n < 2) return;
  while (t < n-t) t += t;
  for (p = t;p > 0;p >>= 1) {
    for (i = 0;i < n-p;++i)
      if (!(i & p))
        minmax(x+i,x+i+p);
    for (q = t;q > p;q >>= 1)
      for (i = 0;i < n-q;++i)
        if (!(i & p))
          minmax(x+i+p,x+i+q);
  }
}

```

How many cycles on, e.g.,
Intel Haswell CPU core?

Every cycle: a vector of 8 32-bit
“min” operations and a vector of
8 32-bit “max” operations.

≥ 3008 cycles for $n = 1024$.

Current software: 7328 cycles.

```

void sort(int32 *x, long long n)
{ long long t,p,q,i;
  t = 1; if (n < 2) return;
  while (t < n-t) t += t;
  for (p = t;p > 0;p >>= 1) {
    for (i = 0;i < n-p;++i)
      if (!(i & p))
        minmax(x+i,x+i+p);
    for (q = t;q > p;q >>= 1)
      for (i = 0;i < n-q;++i)
        if (!(i & p))
          minmax(x+i+p,x+i+q);
  }
}

```

How many cycles on, e.g.,
Intel Haswell CPU core?

Every cycle: a vector of 8 32-bit
“min” operations and a vector of
8 32-bit “max” operations.

≥ 3008 cycles for $n = 1024$.

Current software: 7328 cycles.

(Can gap be narrowed?)

```

void sort(int32 *x, long long n)
{ long long t,p,q,i;
  t = 1; if (n < 2) return;
  while (t < n-t) t += t;
  for (p = t;p > 0;p >>= 1) {
    for (i = 0;i < n-p;++i)
      if (!(i & p))
        minmax(x+i,x+i+p);
    for (q = t;q > p;q >>= 1)
      for (i = 0;i < n-q;++i)
        if (!(i & p))
          minmax(x+i+p,x+i+q);
  }
}

```

How many cycles on, e.g.,
Intel Haswell CPU core?

Every cycle: a vector of 8 32-bit
“min” operations and a vector of
8 32-bit “max” operations.

≥ 3008 cycles for $n = 1024$.

Current software: 7328 cycles.

(Can gap be narrowed?)

This is fastest available sorting
software. Much faster than, e.g.,
Intel’s “Integrated Performance
Primitives” software library.

```

rt(int32 *x, long long n)
long t,p,q,i;
; if (n < 2) return;
(t < n-t) t += t;
p = t;p > 0;p >>= 1) {
(i = 0;i < n-p;++i)
f (!(i & p))
minmax(x+i,x+i+p);
(q = t;q > p;q >>= 1)
or (i = 0;i < n-q;++i)
if (!(i & p))
minmax(x+i+p,x+i+q);

```

How many cycles on, e.g.,
Intel Haswell CPU core?

Every cycle: a vector of 8 32-bit
“min” operations and a vector of
8 32-bit “max” operations.

≥ 3008 cycles for $n = 1024$.

Current software: 7328 cycles.

(Can gap be narrowed?)

This is fastest available sorting
software. Much faster than, e.g.,
Intel’s “Integrated Performance
Primitives” software library.

Constant
“optimiz
code? H

```

*x, long long n)
q, i;
2) return;
) t += t;
0; p >>= 1) {
< n-p; ++i)
p))
+i, x+i+p);
> p; q >>= 1)
; i < n-q; ++i)
& p))
(x+i+p, x+i+q);

```

How many cycles on, e.g.,
Intel Haswell CPU core?

Every cycle: a vector of 8 32-bit
“min” operations and a vector of
8 32-bit “max” operations.

≥ 3008 cycles for $n = 1024$.

Current software: 7328 cycles.

(Can gap be narrowed?)

This is fastest available sorting
software. Much faster than, e.g.,
Intel’s “Integrated Performance
Primitives” software library.

Constant-time code
“optimized” non-c
code? How is this

```
long n)
```

How many cycles on, e.g.,
Intel Haswell CPU core?

```
n;
```

Every cycle: a vector of 8 32-bit
“min” operations and a vector of
8 32-bit “max” operations.

```
1) {
```

```
i)
```

≥ 3008 cycles for $n = 1024$.

Current software: 7328 cycles.

```
;
```

```
= 1)
```

(Can gap be narrowed?)

```
++i)
```

This is fastest available sorting
software. Much faster than, e.g.,
Intel’s “Integrated Performance
Primitives” software library.

```
i+q);
```

Constant-time code faster than
“optimized” non-constant-time
code? How is this possible?

How many cycles on, e.g.,
Intel Haswell CPU core?

Every cycle: a vector of 8 32-bit
“min” operations and a vector of
8 32-bit “max” operations.

≥ 3008 cycles for $n = 1024$.

Current software: 7328 cycles.

(Can gap be narrowed?)

This is fastest available sorting
software. Much faster than, e.g.,
Intel’s “Integrated Performance
Primitives” software library.

Constant-time code faster than
“optimized” non-constant-time
code? How is this possible?

How many cycles on, e.g.,
Intel Haswell CPU core?

Every cycle: a vector of 8 32-bit
“min” operations and a vector of
8 32-bit “max” operations.

≥ 3008 cycles for $n = 1024$.

Current software: 7328 cycles.

(Can gap be narrowed?)

This is fastest available sorting
software. Much faster than, e.g.,
Intel’s “Integrated Performance
Primitives” software library.

Constant-time code faster than
“optimized” non-constant-time
code? How is this possible?

People optimize algorithms
for a naive model of CPUs:

- Branches are fast.
- Random access is fast.

How many cycles on, e.g.,
Intel Haswell CPU core?

Every cycle: a vector of 8 32-bit
“min” operations and a vector of
8 32-bit “max” operations.

≥ 3008 cycles for $n = 1024$.

Current software: 7328 cycles.

(Can gap be narrowed?)

This is fastest available sorting
software. Much faster than, e.g.,
Intel’s “Integrated Performance
Primitives” software library.

Constant-time code faster than
“optimized” non-constant-time
code? How is this possible?

People optimize algorithms
for a naive model of CPUs:

- Branches are fast.
- Random access is fast.

CPUs are evolving
farther and farther away
from this naive model.
Fundamental hardware costs
of constant-time arithmetic are
much lower than random access.

ny cycles on, e.g.,
swell CPU core?

ycle: a vector of 8 32-bit
operations and a vector of
“max” operations.

cycles for $n = 1024$.

software: 7328 cycles.

p be narrowed?)

fastest available sorting

. Much faster than, e.g.,

Integrated Performance

es” software library.

Constant-time code faster than
“optimized” non-constant-time
code? How is this possible?

People optimize algorithms
for a naive model of CPUs:

- Branches are fast.
- Random access is fast.

CPUs are evolving

farther and farther away
from this naive model.

Fundamental hardware costs
of constant-time arithmetic are
much lower than random access.

Modular

Basic EC

add, sub

integers

(Basic M

add, sub

polynom

on, e.g.,
 core?
 tor of 8 32-bit
 and a vector of
 operations.
 $n = 1024$.
 7328 cycles.
 (How?)
 available sorting
 faster than, e.g.,
 Performance
 re library.

Constant-time code faster than
 “optimized” non-constant-time
 code? How is this possible?

People optimize algorithms
 for a naive model of CPUs:

- Branches are fast.
- Random access is fast.

CPUs are evolving
 farther and farther away
 from this naive model.

Fundamental hardware costs
 of constant-time arithmetic are
 much lower than random access.

Modular arithmetic

Basic ECC operations
 add, sub, mul of, e
 integers mod 2^{255}

(Basic NTRU operations
 add, sub, mul of, e
 polynomials mod x)

Constant-time code faster than “optimized” non-constant-time code? How is this possible?

People optimize algorithms for a naive model of CPUs:

- Branches are fast.
- Random access is fast.

CPUs are evolving farther and farther away from this naive model. Fundamental hardware costs of constant-time arithmetic are much lower than random access.

Modular arithmetic

Basic ECC operations: add, sub, mul of, e.g., integers mod $2^{255} - 19$.

(Basic NTRU operations: add, sub, mul of, e.g., polynomials mod $x^{761} - x - 4$)

Constant-time code faster than “optimized” non-constant-time code? How is this possible?

People optimize algorithms for a naive model of CPUs:

- Branches are fast.
- Random access is fast.

CPUs are evolving farther and farther away from this naive model.

Fundamental hardware costs of constant-time arithmetic are much lower than random access.

Modular arithmetic

Basic ECC operations:
add, sub, mul of, e.g.,
integers mod $2^{255} - 19$.

(Basic NTRU operations:
add, sub, mul of, e.g.,
polynomials mod $x^{761} - x - 1$.)

Constant-time code faster than “optimized” non-constant-time code? How is this possible?

People optimize algorithms for a naive model of CPUs:

- Branches are fast.
- Random access is fast.

CPUs are evolving farther and farther away from this naive model.

Fundamental hardware costs of constant-time arithmetic are much lower than random access.

Modular arithmetic

Basic ECC operations:

add, sub, mul of, e.g., integers mod $2^{255} - 19$.

(Basic NTRU operations:

add, sub, mul of, e.g., polynomials mod $x^{761} - x - 1$.)

Typical “big-integer library”:

a variable-length uint32 string

$(f_0, f_1, \dots, f_{\ell-1})$ represents

the nonnegative integer

$$f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}.$$

Uniqueness: $\ell = 0$ or $f_{\ell-1} \neq 0$.

constant-time code faster than
 “naive” non-constant-time
 code. How is this possible?

How to optimize algorithms

Naive model of CPUs:

Cache misses are fast.

Random access is fast.

Cache is evolving

Cache is getting farther away

Cache is a naive model.

Cache is a hardware costs

Cache is a constant-time arithmetic are

Cache is a slower than random access.

Modular arithmetic

Basic ECC operations:

add, sub, mul of, e.g.,

integers mod $2^{255} - 19$.

(Basic NTRU operations:

add, sub, mul of, e.g.,

polynomials mod $x^{761} - x - 1$.)

Typical “big-integer library”:

a variable-length uint32 string

$(f_0, f_1, \dots, f_{\ell-1})$ represents

the nonnegative integer

$$f_0 + 2^{32} f_1 + \dots + 2^{32(\ell-1)} f_{\ell-1}.$$

Uniqueness: $\ell = 0$ or $f_{\ell-1} \neq 0$.

Library p

on this r

$f g; (2)$

le faster than
 constant-time
 possible?
 algorithms
 of CPUs:
 st.
 is fast.
 away
 odel.
 ware costs
 rithmetic are
 random access.

Modular arithmetic

Basic ECC operations:

add, sub, mul of, e.g.,
 integers mod $2^{255} - 19$.

(Basic NTRU operations:

add, sub, mul of, e.g.,
 polynomials mod $x^{761} - x - 1$.)

Typical “big-integer library”:

a variable-length uint32 string

$(f_0, f_1, \dots, f_{\ell-1})$ represents

the nonnegative integer

$$f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}.$$

Uniqueness: $\ell = 0$ or $f_{\ell-1} \neq 0$.

Library provides fu
 on this representat
 fg ; (2) $f, g \mapsto f n$

Modular arithmetic

Basic ECC operations:

add, sub, mul of, e.g.,
integers mod $2^{255} - 19$.

(Basic NTRU operations:

add, sub, mul of, e.g.,
polynomials mod $x^{761} - x - 1$.)

Typical “big-integer library”:

a variable-length uint32 string

$(f_0, f_1, \dots, f_{\ell-1})$ represents

the nonnegative integer

$$f_0 + 2^{32} f_1 + \dots + 2^{32(\ell-1)} f_{\ell-1}.$$

Uniqueness: $\ell = 0$ or $f_{\ell-1} \neq 0$.

Library provides functions ac
on this representation: (1) f
 $f g$; (2) $f, g \mapsto f \bmod g$; etc

Modular arithmetic

Basic ECC operations:

add, sub, mul of, e.g.,
integers mod $2^{255} - 19$.

(Basic NTRU operations:

add, sub, mul of, e.g.,
polynomials mod $x^{761} - x - 1$.)

Typical “big-integer library”:

a variable-length `uint32` string

$(f_0, f_1, \dots, f_{\ell-1})$ represents

the nonnegative integer

$$f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}.$$

Uniqueness: $\ell = 0$ or $f_{\ell-1} \neq 0$.

Library provides functions acting on this representation: (1) $f, g \mapsto fg$; (2) $f, g \mapsto f \bmod g$; etc.

Modular arithmetic

Basic ECC operations:

add, sub, mul of, e.g.,
integers mod $2^{255} - 19$.

(Basic NTRU operations:

add, sub, mul of, e.g.,
polynomials mod $x^{761} - x - 1$.)

Typical “big-integer library”:

a variable-length `uint32` string

$(f_0, f_1, \dots, f_{\ell-1})$ represents

the nonnegative integer

$$f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}.$$

Uniqueness: $\ell = 0$ or $f_{\ell-1} \neq 0$.

Library provides functions acting on this representation: (1) $f, g \mapsto fg$; (2) $f, g \mapsto f \bmod g$; etc.

ECC implementor using library:

multiply $f, g \bmod 2^{255} - 19$

by (1) multiplying f by g ;

(2) reducing mod $2^{255} - 19$.

Modular arithmetic

Basic ECC operations:

add, sub, mul of, e.g.,
integers mod $2^{255} - 19$.

(Basic NTRU operations:

add, sub, mul of, e.g.,
polynomials mod $x^{761} - x - 1$.)

Typical “big-integer library”:

a variable-length `uint32` string

$(f_0, f_1, \dots, f_{\ell-1})$ represents

the nonnegative integer

$$f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}.$$

Uniqueness: $\ell = 0$ or $f_{\ell-1} \neq 0$.

Library provides functions acting on this representation: (1) $f, g \mapsto fg$; (2) $f, g \mapsto f \bmod g$; etc.

ECC implementor using library:

multiply $f, g \bmod 2^{255} - 19$

by (1) multiplying f by g ;

(2) reducing mod $2^{255} - 19$.

But these functions take variable time to ensure uniqueness!

Modular arithmetic

Basic ECC operations:

add, sub, mul of, e.g.,
integers mod $2^{255} - 19$.

(Basic NTRU operations:

add, sub, mul of, e.g.,
polynomials mod $x^{761} - x - 1$.)

Typical “big-integer library”:

a variable-length `uint32` string

$(f_0, f_1, \dots, f_{\ell-1})$ represents

the nonnegative integer

$$f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}.$$

Uniqueness: $\ell = 0$ or $f_{\ell-1} \neq 0$.

Library provides functions acting on this representation: (1) $f, g \mapsto fg$; (2) $f, g \mapsto f \bmod g$; etc.

ECC implementor using library:

multiply $f, g \bmod 2^{255} - 19$

by (1) multiplying f by g ;

(2) reducing mod $2^{255} - 19$.

But these functions take variable time to ensure uniqueness!

Need a different representation for constant-time arithmetic.

Can also gain speed this way.

arithmetic

CC operations:

, mul of, e.g.,

mod $2^{255} - 19$.

TRU operations:

, mul of, e.g.,

ials mod $x^{761} - x - 1$.)

“big-integer library”:

le-length uint32 string

$\dots, f_{\ell-1}$) represents

negative integer

$f_1 + \dots + 2^{32(\ell-1)} f_{\ell-1}$.

ess: $\ell = 0$ or $f_{\ell-1} \neq 0$.

Library provides functions acting on this representation: (1) $f, g \mapsto fg$; (2) $f, g \mapsto f \bmod g$; etc.

ECC implementor using library:

multiply $f, g \bmod 2^{255} - 19$

by (1) multiplying f by g ;

(2) reducing mod $2^{255} - 19$.

But these functions take variable time to ensure uniqueness!

Need a different representation for constant-time arithmetic.

Can also gain speed this way.

Constant

a constant

$(f_0, f_1, \dots$

the nonr

$f_0 + 2^{32}$

Adding t

always a

Don't re

Library provides functions acting on this representation: (1) $f, g \mapsto fg$; (2) $f, g \mapsto f \bmod g$; etc.

ECC implementor using library:
multiply $f, g \bmod 2^{255} - 19$
by (1) multiplying f by g ;
(2) reducing mod $2^{255} - 19$.

But these functions take variable time to ensure uniqueness!

Need a different representation for constant-time arithmetic.
Can also gain speed this way.

Constant-time big
a constant-length
 $(f_0, f_1, \dots, f_{\ell-1})$ re
the nonnegative in
 $f_0 + 2^{32}f_1 + \dots +$
Adding two ℓ -limb
always allocate $\ell +$
Don't remove top

Library provides functions acting on this representation: (1) $f, g \mapsto fg$; (2) $f, g \mapsto f \bmod g$; etc.

ECC implementor using library:
multiply $f, g \bmod 2^{255} - 19$
by (1) multiplying f by g ;
(2) reducing mod $2^{255} - 19$.

But these functions take variable time to ensure uniqueness!

Need a different representation for constant-time arithmetic.

Can also gain speed this way.

Constant-time bigint library:
a constant-length `uint32` string
 $(f_0, f_1, \dots, f_{\ell-1})$ represents
the nonnegative integer
 $f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}$.

Adding two ℓ -limb integers:
always allocate $\ell + 1$ limbs.
Don't remove top zero limb.

Library provides functions acting on this representation: (1) $f, g \mapsto fg$; (2) $f, g \mapsto f \bmod g$; etc.

ECC implementor using library:
multiply $f, g \bmod 2^{255} - 19$
by (1) multiplying f by g ;
(2) reducing mod $2^{255} - 19$.

But these functions take variable time to ensure uniqueness!

Need a different representation for constant-time arithmetic.
Can also gain speed this way.

Constant-time bigint library:
a constant-length uint32 string $(f_0, f_1, \dots, f_{\ell-1})$ represents the nonnegative integer $f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}$.

Adding two ℓ -limb integers:
always allocate $\ell + 1$ limbs.
Don't remove top zero limb.

Library provides functions acting on this representation: (1) $f, g \mapsto fg$; (2) $f, g \mapsto f \bmod g$; etc.

ECC implementor using library:
multiply $f, g \bmod 2^{255} - 19$
by (1) multiplying f by g ;
(2) reducing mod $2^{255} - 19$.

But these functions take variable time to ensure uniqueness!

Need a different representation for constant-time arithmetic.

Can also gain speed this way.

Constant-time bigint library:
a constant-length uint32 string $(f_0, f_1, \dots, f_{\ell-1})$ represents the nonnegative integer $f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}$.

Adding two ℓ -limb integers:
always allocate $\ell + 1$ limbs.
Don't remove top zero limb.

Can also track bounds more refined than $2^0, 2^{32}, 2^{64}, 2^{96}, \dots$;
but no limbs \rightarrow bounds data flow.

Library provides functions acting on this representation: (1) $f, g \mapsto fg$; (2) $f, g \mapsto f \bmod g$; etc.

ECC implementor using library:
multiply $f, g \bmod 2^{255} - 19$
by (1) multiplying f by g ;
(2) reducing mod $2^{255} - 19$.

But these functions take variable time to ensure uniqueness!

Need a different representation for constant-time arithmetic.

Can also gain speed this way.

Constant-time bigint library:
a constant-length uint32 string $(f_0, f_1, \dots, f_{\ell-1})$ represents the nonnegative integer $f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}$.

Adding two ℓ -limb integers:
always allocate $\ell + 1$ limbs.
Don't remove top zero limb.

Can also track bounds more refined than $2^0, 2^{32}, 2^{64}, 2^{96}, \dots$;
but no limbs \rightarrow bounds data flow.

$f \bmod p$ is as short as p .

provides functions acting
 representation: (1) $f, g \mapsto$
 $f, g \mapsto f \bmod g$; etc.

complementor using library:

$f, g \bmod 2^{255} - 19$

multiplying f by g ;

reducing mod $2^{255} - 19$.

These functions take variable
 lengths to ensure uniqueness!

Use a different representation

for constant-time arithmetic.

to gain speed this way.

Constant-time bigint library:
 a constant-length uint32 string
 $(f_0, f_1, \dots, f_{\ell-1})$ represents
 the nonnegative integer
 $f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}$.

Adding two ℓ -limb integers:
 always allocate $\ell + 1$ limbs.
 Don't remove top zero limb.

Can also track bounds more
 refined than $2^0, 2^{32}, 2^{64}, 2^{96}, \dots$;
 but no limbs \rightarrow bounds data flow.

$f \bmod p$ is as short as p .

Usually \dots
 uint32
 represent
 $2^{77}f_3 + \dots$
 $2^{179}f_7 + \dots$

Constant
 More limbs
 but save
 overflow

After mu
 replace 2

functions acting
 tion: (1) $f, g \mapsto$
 $\text{mod } g$; etc.

using library:
 $2^{255} - 19$

f by g ;
 $2^{255} - 19$.

ns take variable
 queness!

representation
 arithmetic.

ed this way.

Constant-time bigint library:
 a constant-length uint32 string
 $(f_0, f_1, \dots, f_{\ell-1})$ represents
 the nonnegative integer
 $f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}$.

Adding two ℓ -limb integers:
 always allocate $\ell + 1$ limbs.
 Don't remove top zero limb.

Can also track bounds more
 refined than $2^0, 2^{32}, 2^{64}, 2^{96}, \dots$;
 but no limbs \rightarrow bounds data flow.

$f \text{ mod } p$ is as short as p .

Usually faster repr
 uint32 string $(f_0,$
 represents $f_0 + 2^{32}f_1 +$
 $2^{77}f_3 + 2^{102}f_4 + 2^{127}f_5 +$
 $2^{179}f_7 + 2^{204}f_8 + 2^{229}f_9 +$

Constant bound o
 More limbs than b
 but save time by a
 overflows and dela

After multiplicatio
 replace 2^{255} with

Constant-time bigint library:
 a constant-length uint32 string
 $(f_0, f_1, \dots, f_{\ell-1})$ represents
 the nonnegative integer
 $f_0 + 2^{32} f_1 + \dots + 2^{32(\ell-1)} f_{\ell-1}$.

Adding two ℓ -limb integers:
 always allocate $\ell + 1$ limbs.
 Don't remove top zero limb.

Can also track bounds more
 refined than $2^0, 2^{32}, 2^{64}, 2^{96}, \dots$;
 but no limbs \rightarrow bounds data flow.

$f \bmod p$ is as short as p .

Usually faster representation
 uint32 string (f_0, f_1, \dots, f_9)
 represents $f_0 + 2^{26} f_1 + 2^{51} f_2 +$
 $2^{77} f_3 + 2^{102} f_4 + 2^{128} f_5 + 2^{154}$
 $2^{179} f_7 + 2^{204} f_8 + 2^{230} f_9$.

Constant bound on each f_i .

More limbs than before,
 but save time by avoiding
 overflows and delaying carries.

After multiplication,
 replace 2^{255} with 19.

Constant-time bigint library:
 a constant-length uint32 string
 $(f_0, f_1, \dots, f_{\ell-1})$ represents
 the nonnegative integer
 $f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}$.

Adding two ℓ -limb integers:
 always allocate $\ell + 1$ limbs.
 Don't remove top zero limb.
 Can also track bounds more
 refined than $2^0, 2^{32}, 2^{64}, 2^{96}, \dots$;
 but no limbs \rightarrow bounds data flow.
 $f \bmod p$ is as short as p .

Usually faster representation:
 uint32 string (f_0, f_1, \dots, f_9)
 represents $f_0 + 2^{26}f_1 + 2^{51}f_2 +$
 $2^{77}f_3 + 2^{102}f_4 + 2^{128}f_5 + 2^{153}f_6 +$
 $2^{179}f_7 + 2^{204}f_8 + 2^{230}f_9$.

Constant bound on each f_i .

More limbs than before,
 but save time by avoiding
 overflows and delaying carries.

After multiplication,
 replace 2^{255} with 19.

Constant-time bigint library:
 a constant-length uint32 string
 $(f_0, f_1, \dots, f_{\ell-1})$ represents
 the nonnegative integer
 $f_0 + 2^{32}f_1 + \dots + 2^{32(\ell-1)}f_{\ell-1}$.

Adding two ℓ -limb integers:
 always allocate $\ell + 1$ limbs.
 Don't remove top zero limb.
 Can also track bounds more
 refined than $2^0, 2^{32}, 2^{64}, 2^{96}, \dots$;
 but no limbs \rightarrow bounds data flow.
 $f \bmod p$ is as short as p .

Usually faster representation:
 uint32 string (f_0, f_1, \dots, f_9)
 represents $f_0 + 2^{26}f_1 + 2^{51}f_2 +$
 $2^{77}f_3 + 2^{102}f_4 + 2^{128}f_5 + 2^{153}f_6 +$
 $2^{179}f_7 + 2^{204}f_8 + 2^{230}f_9$.

Constant bound on each f_i .

More limbs than before,
 but save time by avoiding
 overflows and delaying carries.

After multiplication,
 replace 2^{255} with 19.

Slightly faster on some CPUs:
 int32 string (f_0, f_1, \dots, f_9) .

fast-time bigint library:

uint-length uint32 string

$(f_0, \dots, f_{\ell-1})$ represents

negative integer

$$-f_1 + \dots + 2^{32(\ell-1)} f_{\ell-1}.$$

two ℓ -limb integers:

allocate $\ell + 1$ limbs.

remove top zero limb.

to track bounds more

than $2^0, 2^{32}, 2^{64}, 2^{96}, \dots$;

limbs \rightarrow bounds data flow.

is as short as p .

Usually faster representation:

uint32 string (f_0, f_1, \dots, f_9)

represents $f_0 + 2^{26} f_1 + 2^{51} f_2 +$

$2^{77} f_3 + 2^{102} f_4 + 2^{128} f_5 + 2^{153} f_6 +$

$2^{179} f_7 + 2^{204} f_8 + 2^{230} f_9.$

Constant bound on each f_i .

More limbs than before,

but save time by avoiding

overflows and delaying carries.

After multiplication,

replace 2^{255} with 19.

Slightly faster on some CPUs:

uint32 string $(f_0, f_1, \dots, f_9).$

int32 f0

int32 g0

...

int64 f1

int64 f2

f7_2

...

int64 h4

...

c4 = (h4

h5 += c4

uint library:
 uint32 string
 represents
 integer
 $2^{32(\ell-1)} f_{\ell-1}$.
 integers:
 + 1 limbs.
 zero limb.
 bounds more
 $2, 2^{64}, 2^{96}, \dots$;
 bounds data flow.
 t as p .

Usually faster representation:
 uint32 string (f_0, f_1, \dots, f_9)
 represents $f_0 + 2^{26} f_1 + 2^{51} f_2 +$
 $2^{77} f_3 + 2^{102} f_4 + 2^{128} f_5 + 2^{153} f_6 +$
 $2^{179} f_7 + 2^{204} f_8 + 2^{230} f_9$.

Constant bound on each f_i .

More limbs than before,
 but save time by avoiding
 overflows and delaying carries.

After multiplication,
 replace 2^{255} with 19.

Slightly faster on some CPUs:
 uint32 string (f_0, f_1, \dots, f_9) .

```
int32 f7_2 = 2 *
int32 g7_19 = 19
...
int64 f0g4 = f0
int64 f7g7_38 =
    f7_2 * (int64)
...
int64 h4 = f0g4
           + f2g2
           + f4g0
           + f6g8_
           + f8g6_
...
c4 = (h4 + (int6
h5 += c4; h4 -=
```

Usually faster representation:

`uint32 string (f0, f1, ..., f9)`
 represents $f_0 + 2^{26}f_1 + 2^{51}f_2 + 2^{77}f_3 + 2^{102}f_4 + 2^{128}f_5 + 2^{153}f_6 + 2^{179}f_7 + 2^{204}f_8 + 2^{230}f_9$.

Constant bound on each f_i .

More limbs than before,
 but save time by avoiding
 overflows and delaying carries.

After multiplication,
 replace 2^{255} with 19.

Slightly faster on some CPUs:

`uint32 string (f0, f1, ..., f9)`.

```
int32 f7_2 = 2 * f7;
int32 g7_19 = 19 * g7;
...
int64 f0g4 = f0 * (int64)
int64 f7g7_38 =
    f7_2 * (int64) g7_19;
...
int64 h4 = f0g4 + f1g3_2
          + f2g2 + f3g1_2
          + f4g0 + f5g9_38
          + f6g8_19 + f7g7
          + f8g6_19 + f9g5
...
c4 = (h4 + (int64)(1<<25))
h5 += c4; h4 -= c4 << 26;
```

Usually faster representation:

`uint32 string (f0, f1, ..., f9)`
 represents $f_0 + 2^{26}f_1 + 2^{51}f_2 + 2^{77}f_3 + 2^{102}f_4 + 2^{128}f_5 + 2^{153}f_6 + 2^{179}f_7 + 2^{204}f_8 + 2^{230}f_9$.

Constant bound on each f_i .

More limbs than before,
 but save time by avoiding
 overflows and delaying carries.

After multiplication,
 replace 2^{255} with 19.

Slightly faster on some CPUs:
`int32 string (f0, f1, ..., f9)`.

```
int32 f7_2 = 2 * f7;
int32 g7_19 = 19 * g7;
...
int64 f0g4 = f0 * (int64) g4;
int64 f7g7_38 =
    f7_2 * (int64) g7_19;
...
int64 h4 = f0g4 + f1g3_2
          + f2g2 + f3g1_2
          + f4g0 + f5g9_38
          + f6g8_19 + f7g7_38
          + f8g6_19 + f9g5_38;
...
c4 = (h4 + (int64)(1<<25)) >> 26;
h5 += c4; h4 -= c4 << 26;
```

faster representation:

string (f_0, f_1, \dots, f_9)

ints $f_0 + 2^{26}f_1 + 2^{51}f_2 +$
 $2^{102}f_4 + 2^{128}f_5 + 2^{153}f_6 +$
 $2^{204}f_8 + 2^{230}f_9.$

at bound on each f_i .

bits than before,

time by avoiding

and delaying carries.

multiplication,

2^{255} with 19.

faster on some CPUs:

string $(f_0, f_1, \dots, f_9).$

```
int32 f7_2 = 2 * f7;
int32 g7_19 = 19 * g7;
...
int64 f0g4 = f0 * (int64) g4;
int64 f7g7_38 =
    f7_2 * (int64) g7_19;
...
int64 h4 = f0g4 + f1g3_2
          + f2g2 + f3g1_2
          + f4g0 + f5g9_38
          + f6g8_19 + f7g7_38
          + f8g6_19 + f9g5_38;
...
c4 = (h4 + (int64)(1<<25)) >> 26;
h5 += c4; h4 -= c4 << 26;
```

Initial co

is polyno

modulo

Exercise

are being

presentation:

$$(f_1, \dots, f_9)$$

$$2^6 f_1 + 2^{51} f_2 +$$

$$2^{128} f_5 + 2^{153} f_6 +$$

$$2^{230} f_9.$$

in each f_i .

before,

avoiding

shifting carries.

in,

19.

on some CPUs:

$$(f_1, \dots, f_9).$$

```
int32 f7_2 = 2 * f7;
int32 g7_19 = 19 * g7;
...
int64 f0g4 = f0 * (int64) g4;
int64 f7g7_38 =
    f7_2 * (int64) g7_19;
...
int64 h4 = f0g4 + f1g3_2
          + f2g2 + f3g1_2
          + f4g0 + f5g9_38
          + f6g8_19 + f7g7_38
          + f8g6_19 + f9g5_38;
...
c4 = (h4 + (int64)(1<<25)) >> 26;
h5 += c4; h4 -= c4 << 26;
```

Initial computation

is polynomial mult

modulo $x^{10} - 19$.

Exercise: Which p

are being multiplie

34

```

int32 f7_2 = 2 * f7;
int32 g7_19 = 19 * g7;
...
int64 f0g4 = f0 * (int64) g4;
int64 f7g7_38 =
    f7_2 * (int64) g7_19;
...
int64 h4 = f0g4 + f1g3_2
          + f2g2 + f3g1_2
          + f4g0 + f5g9_38
          + f6g8_19 + f7g7_38
          + f8g6_19 + f9g5_38;
...
c4 = (h4 + (int64)(1<<25)) >> 26;
h5 += c4; h4 -= c4 << 26;

```

35

Initial computation of h_0, \dots
 is polynomial multiplication
 modulo $x^{10} - 19$.

Exercise: Which polynomials
 are being multiplied?


```

int32 f7_2 = 2 * f7;
int32 g7_19 = 19 * g7;
...
int64 f0g4 = f0 * (int64) g4;
int64 f7g7_38 =
    f7_2 * (int64) g7_19;
...
int64 h4 = f0g4 + f1g3_2
          + f2g2 + f3g1_2
          + f4g0 + f5g9_38
          + f6g8_19 + f7g7_38
          + f8g6_19 + f9g5_38;
...
c4 = (h4 + (int64)(1<<25)) >> 26;
h5 += c4; h4 -= c4 << 26;

```

Initial computation of h_0, \dots, h_9 is polynomial multiplication modulo $x^{10} - 19$.

Exercise: Which polynomials are being multiplied?

```

int32 f7_2 = 2 * f7;
int32 g7_19 = 19 * g7;
...
int64 f0g4 = f0 * (int64) g4;
int64 f7g7_38 =
    f7_2 * (int64) g7_19;
...
int64 h4 = f0g4 + f1g3_2
          + f2g2 + f3g1_2
          + f4g0 + f5g9_38
          + f6g8_19 + f7g7_38
          + f8g6_19 + f9g5_38;
...
c4 = (h4 + (int64)(1<<25)) >> 26;
h5 += c4; h4 -= c4 << 26;

```

Initial computation of h_0, \dots, h_9 is polynomial multiplication modulo $x^{10} - 19$.

Exercise: Which polynomials are being multiplied?

Reduction modulo $x^{10} - 19$ and carries such as $h_4 \rightarrow h_5$ **squeeze** the product into limited-size representation suitable for next multiplication.

```

int32 f7_2 = 2 * f7;
int32 g7_19 = 19 * g7;
...
int64 f0g4 = f0 * (int64) g4;
int64 f7g7_38 =
    f7_2 * (int64) g7_19;
...
int64 h4 = f0g4 + f1g3_2
          + f2g2 + f3g1_2
          + f4g0 + f5g9_38
          + f6g8_19 + f7g7_38
          + f8g6_19 + f9g5_38;
...
c4 = (h4 + (int64)(1<<25)) >> 26;
h5 += c4; h4 -= c4 << 26;

```

Initial computation of h_0, \dots, h_9 is polynomial multiplication modulo $x^{10} - 19$.

Exercise: Which polynomials are being multiplied?

Reduction modulo $x^{10} - 19$ and carries such as $h_4 \rightarrow h_5$
squeeze the product into limited-size representation suitable for next multiplication.

At end of computation:
freeze representation into unique representation suitable for network transmission.

```

7_2 = 2 * f7;
7_19 = 19 * g7;

0g4 = f0 * (int64) g4;
7g7_38 =
* (int64) g7_19;

4 = f0g4 + f1g3_2
+ f2g2 + f3g1_2
+ f4g0 + f5g9_38
+ f6g8_19 + f7g7_38
+ f8g6_19 + f9g5_38;

4 + (int64)(1<<25) >> 26;
4; h4 -= c4 << 26;

```

Initial computation of h_0, \dots, h_9 is polynomial multiplication modulo $x^{10} - 19$.

Exercise: Which polynomials are being multiplied?

Reduction modulo $x^{10} - 19$ and carries such as $h_4 \rightarrow h_5$ **squeeze** the product into limited-size representation suitable for next multiplication.

At end of computation: **freeze** representation into unique representation suitable for network transmission.

Much more
see, e.g.

```

f7;
* g7;

* (int64) g4;

g7_19;

+ f1g3_2
+ f3g1_2
+ f5g9_38
19 + f7g7_38
19 + f9g5_38;

4) (1<<25)) >> 26;
c4 << 26;

```

Initial computation of h_0, \dots, h_9 is polynomial multiplication modulo $x^{10} - 19$.

Exercise: Which polynomials are being multiplied?

Reduction modulo $x^{10} - 19$ and carries such as $h_4 \rightarrow h_5$ **squeeze** the product into limited-size representation suitable for next multiplication.

At end of computation: **freeze** representation into unique representation suitable for network transmission.

Much more about
see, e.g., [2015 Ch](#)

Initial computation of h_0, \dots, h_9
is polynomial multiplication
modulo $x^{10} - 19$.

Exercise: Which polynomials
are being multiplied?

Reduction modulo $x^{10} - 19$
and carries such as $h_4 \rightarrow h_5$
squeeze the product
into limited-size representation
suitable for next multiplication.

At end of computation:
freeze representation
into unique representation
suitable for network transmission.

Much more about ECC speed
see, e.g., [2015 Chou](#).

Initial computation of h_0, \dots, h_9
is polynomial multiplication
modulo $x^{10} - 19$.

Exercise: Which polynomials
are being multiplied?

Reduction modulo $x^{10} - 19$
and carries such as $h_4 \rightarrow h_5$
squeeze the product
into limited-size representation
suitable for next multiplication.

At end of computation:
freeze representation
into unique representation
suitable for network transmission.

Much more about ECC speed:
see, e.g., [2015 Chou](#).

Initial computation of h_0, \dots, h_9
is polynomial multiplication
modulo $x^{10} - 19$.

Exercise: Which polynomials
are being multiplied?

Reduction modulo $x^{10} - 19$
and carries such as $h_4 \rightarrow h_5$
squeeze the product
into limited-size representation
suitable for next multiplication.

At end of computation:
freeze representation
into unique representation
suitable for network transmission.

Much more about ECC speed:
see, e.g., [2015 Chou](#).

Verifying constant time:
increasingly automated.

Initial computation of h_0, \dots, h_9
is polynomial multiplication
modulo $x^{10} - 19$.

Exercise: Which polynomials
are being multiplied?

Reduction modulo $x^{10} - 19$
and carries such as $h_4 \rightarrow h_5$
squeeze the product
into limited-size representation
suitable for next multiplication.

At end of computation:
freeze representation
into unique representation
suitable for network transmission.

Much more about ECC speed:
see, e.g., [2015 Chou](#).

Verifying constant time:
increasingly automated.

Testing can miss rare bugs
that attacker might trigger.
Fix: prove that software
matches mathematical spec;
have computer check proofs.

Initial computation of h_0, \dots, h_9
is polynomial multiplication
modulo $x^{10} - 19$.

Exercise: Which polynomials
are being multiplied?

Reduction modulo $x^{10} - 19$
and carries such as $h_4 \rightarrow h_5$
squeeze the product
into limited-size representation
suitable for next multiplication.

At end of computation:
freeze representation
into unique representation
suitable for network transmission.

Much more about ECC speed:
see, e.g., [2015 Chou](#).

Verifying constant time:
increasingly automated.

Testing can miss rare bugs
that attacker might trigger.
Fix: prove that software
matches mathematical spec;
have computer check proofs.

Progress in deploying proven
fast software: see, e.g., 2015
Bernstein–Schwabe [“gfverif”](#);
2017 [HAACL* X25519 in Firefox](#).

computation of h_0, \dots, h_9
 polynomial multiplication
 $x^{10} - 19$.
 Which polynomials
 multiplied?
 modulo $x^{10} - 19$
 such as $h_4 \rightarrow h_5$
 the product
 fixed-size representation
 for next multiplication.
 of computation:
 representation
 unique representation
 for network transmission.

Much more about ECC speed:
 see, e.g., [2015 Chou](#).

Verifying constant time:
 increasingly automated.

Testing can miss rare bugs
 that attacker might trigger.
 Fix: prove that software
 matches mathematical spec;
 have computer check proofs.

Progress in deploying proven
 fast software: see, e.g., [2015](#)
[Bernstein–Schwabe “gfverif”](#) ;
[2017 HAACL* X25519 in Firefox](#).

gfverif h
 impleme
 plus occ
 against t

 $p = 2^{**2}$
 $A = 4860$
 x_2, z_2, x_3
 for i in
 $n_i = 1$
 x_2, x_3
 z_2, z_3
 x_3, z_3
 $4 * x_1$
 x_2, z_2
 $4 * x_2$

Much more about ECC speed:
see, e.g., [2015 Chou](#).

Verifying constant time:
increasingly automated.

Testing can miss rare bugs
that attacker might trigger.
Fix: prove that software
matches mathematical spec;
have computer check proofs.

Progress in deploying proven
fast software: see, e.g., 2015
Bernstein–Schwabe “[gfverif](#)”;
2017 [HAACL* X25519 in Firefox](#).

gfverif has verified
implementation of
plus occasional an
against the followi

```
p = 2**255-19
A = 486662
x2,z2,x3,z3 = 1,
for i in reverse
    ni = bit(n,i)
    x2,x3 = cswap(
    z2,z3 = cswap(
    x3,z3 = (4*(x2
        4*x1*(x2*z3-z
    x2,z2 = ((x2**
        4*x2*z2*(x2**
```

. , h9

Much more about ECC speed:
see, e.g., [2015 Chou](#).

Verifying constant time:
increasingly automated.

Testing can miss rare bugs
that attacker might trigger.

Fix: prove that software
matches mathematical spec;
have computer check proofs.

on
on.

Progress in deploying proven
fast software: see, e.g., [2015
Bernstein–Schwabe “gfverif”](#) ;
[2017 HACL* X25519 in Firefox](#).

ssion.

gfverif has verified ref10
implementation of X25519,
plus occasional annotations,
against the following specific

```
p = 2**255-19
A = 486662
x2,z2,x3,z3 = 1,0,x1,1
for i in reversed(range(255)):
    ni = bit(n,i)
    x2,x3 = cswap(x2,x3,ni)
    z2,z3 = cswap(z2,z3,ni)
    x3,z3 = (4*(x2*x3-z2*z3)
             + 4*x1*(x2*z3-z2*x3)**2)
    x2,z2 = ((x2**2-z2**2)*
             + 4*x2*z2*(x2**2+A*x2*z2
```

Much more about ECC speed:
see, e.g., [2015 Chou](#).

Verifying constant time:
increasingly automated.

Testing can miss rare bugs
that attacker might trigger.
Fix: prove that software
matches mathematical spec;
have computer check proofs.

Progress in deploying proven
fast software: see, e.g., [2015
Bernstein–Schwabe “gfverif”](#);
[2017 HAACL* X25519 in Firefox](#).

gfverif has verified ref10
implementation of X25519,
plus occasional annotations,
against the following specification:

```
p = 2**255-19
A = 486662
x2,z2,x3,z3 = 1,0,x1,1
for i in reversed(range(255)):
    ni = bit(n,i)
    x2,x3 = cswap(x2,x3,ni)
    z2,z3 = cswap(z2,z3,ni)
    x3,z3 = (4*(x2*x3-z2*z3)**2,
             4*x1*(x2*z3-z2*x3)**2)
    x2,z2 = ((x2**2-z2**2)**2,
             4*x2*z2*(x2**2+A*x2*z2+z2**2))
```


more about ECC speed:
[2015 Chou](#).

g constant time:
 ngly automated.

can miss rare bugs
 acker might trigger.

ve that software
 mathematical spec;
 mputer check proofs.

s in deploying proven
 ware: see, e.g., 2015

n-Schwabe [“gfverif”](#);

[ACL* X25519 in Firefox](#).

gfverif has verified ref10
 implementation of X25519,
 plus occasional annotations,
 against the following specification:

```
p = 2**255-19
A = 486662
x2,z2,x3,z3 = 1,0,x1,1
for i in reversed(range(255)):
    ni = bit(n,i)
    x2,x3 = cswap(x2,x3,ni)
    z2,z3 = cswap(z2,z3,ni)
    x3,z3 = (4*(x2*x3-z2*z3)**2,
             4*x1*(x2*z3-z2*x3)**2)
    x2,z2 = ((x2**2-z2**2)**2,
             4*x2*z2*(x2**2+A*x2*z2+z2**2))
```

x3,z3

x2,z2

cut(x3)

cut(x2)

cut(z3)

cut(z2)

x2,x3

z2,z3

cut(x2)

cut(z2)

return

What's v

is the sa

and is b

ECC speed:

ou.

time:

nated.

are bugs

nt trigger.

ftware

tical spec;

eck proofs.

ing proven

e.g., 2015

e “gfverif”;

519 in Firefox.

gfverif has verified ref10
implementation of X25519,
plus occasional annotations,
against the following specification:

```
p = 2**255-19
A = 486662
x2,z2,x3,z3 = 1,0,x1,1
for i in reversed(range(255)):
    ni = bit(n,i)
    x2,x3 = cswap(x2,x3,ni)
    z2,z3 = cswap(z2,z3,ni)
    x3,z3 = (4*(x2*x3-z2*z3)**2,
             4*x1*(x2*z3-z2*x3)**2)
    x2,z2 = ((x2**2-z2**2)**2,
             4*x2*z2*(x2**2+A*x2*z2+z2**2))
```

x3,z3 = (x3%p,

x2,z2 = (x2%p,

cut(x2)

cut(x3)

cut(z2)

cut(z3)

x2,x3 = cswap(

z2,z3 = cswap(

cut(x2)

cut(z2)

return x2*pow(z2

What's verified: o

is the same as spe

and is between 0 a

ed:

gfverif has verified ref10
implementation of X25519,
plus occasional annotations,
against the following specification:

```
p = 2**255-19
A = 486662
x2,z2,x3,z3 = 1,0,x1,1
for i in reversed(range(255)):
    ni = bit(n,i)
    x2,x3 = cswap(x2,x3,ni)
    z2,z3 = cswap(z2,z3,ni)
    x3,z3 = (4*(x2*x3-z2*z3)**2,
             4*x1*(x2*z3-z2*x3)**2)
    x2,z2 = ((x2**2-z2**2)**2,
             4*x2*z2*(x2**2+A*x2*z2+z2**2))
```

```
x3,z3 = (x3%p,z3%p)
x2,z2 = (x2%p,z2%p)
cut(x2)
cut(x3)
cut(z2)
cut(z3)
x2,x3 = cswap(x2,x3,ni)
z2,z3 = cswap(z2,z3,ni)
cut(x2)
cut(z2)
return x2*pow(z2,p-2,p)
```

What's verified: output of r
is the same as spec mod p ,
and is between 0 and $p - 1$.

efox.

gfverif has verified ref10
implementation of X25519,
plus occasional annotations,
against the following specification:

```
p = 2**255-19
A = 486662
x2,z2,x3,z3 = 1,0,x1,1
for i in reversed(range(255)):
    ni = bit(n,i)
    x2,x3 = cswap(x2,x3,ni)
    z2,z3 = cswap(z2,z3,ni)
    x3,z3 = (4*(x2*x3-z2*z3)**2,
             4*x1*(x2*z3-z2*x3)**2)
    x2,z2 = ((x2**2-z2**2)**2,
             4*x2*z2*(x2**2+A*x2*z2+z2**2))
```

```
x3,z3 = (x3%p,z3%p)
x2,z2 = (x2%p,z2%p)
cut(x2)
cut(x3)
cut(z2)
cut(z3)
x2,x3 = cswap(x2,x3,ni)
z2,z3 = cswap(z2,z3,ni)
cut(x2)
cut(z2)
return x2*pow(z2,p-2,p)
```

What's verified: output of ref10
is the same as spec mod p ,
and is between 0 and $p - 1$.

as verified ref10
 ntation of X25519,
 asional annotations,
 the following specification:

```

255-19
662
x3,z3 = 1,0,x1,1
n reversed(range(255)):
bit(n,i)
= cswap(x2,x3,ni)
= cswap(z2,z3,ni)
= (4*(x2*x3-z2*z3)**2,
*(x2*z3-z2*x3)**2)
= ((x2**2-z2**2)**2,
*z2*(x2**2+A*x2*z2+z2**2))

```

“What a

NIST P-
 $2^{256} - 2$

ECDSA
 reductio
 an integ

Write A
 $(A_{15}, A_{14}, \dots, A_8, A_7,$
 meaning

Define
 $T; S_1; S_2$
 as

```

x3,z3 = (x3%p,z3%p)
x2,z2 = (x2%p,z2%p)
cut(x2)
cut(x3)
cut(z2)
cut(z3)
x2,x3 = cswap(x2,x3,ni)
z2,z3 = cswap(z2,z3,ni)
cut(x2)
cut(z2)
return x2*pow(z2,p-2,p)

```

What's verified: output of ref10
 is the same as spec mod p ,
 and is between 0 and $p - 1$.

```

ref10
X25519,
notations,
ng specification:

0, x1, 1
d(range(255)) :

x2, x3, ni)
z2, z3, ni)
*x3-z2*z3)**2,
2*x3)**2)
2-z2**2)**2,
2+A*x2*z2+z2**2))

```

```

x3, z3 = (x3%p, z3%p)
x2, z2 = (x2%p, z2%p)
cut(x2)
cut(x3)
cut(z2)
cut(z3)
x2, x3 = cswap(x2, x3, ni)
z2, z3 = cswap(z2, z3, ni)
cut(x2)
cut(z2)
return x2*pow(z2, p-2, p)

```

What's verified: output of ref10
is the same as spec mod p ,
and is between 0 and $p - 1$.

“What a difference

NIST P-256 prime
 $2^{256} - 2^{224} + 2^{192}$

ECDSA standard s
reduction procedur
an integer “A less

Write A as

$(A_{15}, A_{14}, A_{13}, A_{12},$
 $A_8, A_7, A_6, A_5, A_4,$
meaning $\sum_i A_i 2^{32i}$

Define

$T; S_1; S_2; S_3; S_4; D$
as

```

x3, z3 = (x3%p, z3%p)
x2, z2 = (x2%p, z2%p)
cut(x2)
cut(x3)
cut(z2)
cut(z3)
x2, x3 = cswap(x2, x3, ni)
z2, z3 = cswap(z2, z3, ni)
cut(x2)
cut(z2)
return x2*pow(z2, p-2, p)

```

What's verified: output of ref10
is the same as spec mod p ,
and is between 0 and $p - 1$.

“What a difference a prime

NIST P-256 prime p is
 $2^{256} - 2^{224} + 2^{192} + 2^{96} - 1$

ECDSA standard specifies
reduction procedure given
an integer “ A less than p^2 ”:

Write A as

$(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, A_{10},$
 $A_8, A_7, A_6, A_5, A_4, A_3, A_2,$
meaning $\sum_i A_i 2^{32i}$.

Define

$T; S_1; S_2; S_3; S_4; D_1; D_2; D_3$
as

```

x3,z3 = (x3%p,z3%p)
x2,z2 = (x2%p,z2%p)
cut(x2)
cut(x3)
cut(z2)
cut(z3)
x2,x3 = cswap(x2,x3,ni)
z2,z3 = cswap(z2,z3,ni)
cut(x2)
cut(z2)
return x2*pow(z2,p-2,p)

```

What's verified: output of ref10
is the same as spec mod p ,
and is between 0 and $p - 1$.

"What a difference a prime makes"

NIST P-256 prime p is
 $2^{256} - 2^{224} + 2^{192} + 2^{96} - 1$.

ECDSA standard specifies
reduction procedure given
an integer "A less than p^2 ":

Write A as

$(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, A_{10}, A_9,$
 $A_8, A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0)$,
meaning $\sum_i A_i 2^{32i}$.

Define

$T; S_1; S_2; S_3; S_4; D_1; D_2; D_3; D_4$
as

= (x3%p, z3%p)

= (x2%p, z2%p)

2)

3)

2)

3)

= cswap(x2, x3, ni)

= cswap(z2, z3, ni)

x2*pow(z2, p-2, p)

verified: output of ref10

me as spec mod p ,

between 0 and $p - 1$.

“What a difference a prime makes”

NIST P-256 prime p is

$$2^{256} - 2^{224} + 2^{192} + 2^{96} - 1.$$

ECDSA standard specifies

reduction procedure given

an integer “ A less than p^2 ”:

Write A as

$$(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, A_{10}, A_9, \\ A_8, A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0),$$

meaning $\sum_i A_i 2^{32i}$.

Define

$$T; S_1; S_2; S_3; S_4; D_1; D_2; D_3; D_4$$

as

($A_7, A_6,$

($A_{15}, A_{14},$

(0, $A_{15},$

($A_{15}, A_{14},$

($A_8, A_{13},$

($A_{10}, A_8,$

($A_{11}, A_9,$

($A_{12}, 0,$

($A_{13}, 0,$

Compute

$S_4 - D_1$

Reduce

subtract

$z^3 \pmod p$)

$z^2 \pmod p$)

(x_2, x_3, n_i)

(z_2, z_3, n_i)

$(, p-2, p)$

output of ref10

$c \pmod p$,

and $p - 1$.

“What a difference a prime makes”

NIST P-256 prime p is

$$2^{256} - 2^{224} + 2^{192} + 2^{96} - 1.$$

ECDSA standard specifies

reduction procedure given

an integer “ A less than p^2 ”:

Write A as

$$(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, A_{10}, A_9, \\ A_8, A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0),$$

meaning $\sum_i A_i 2^{32i}$.

Define

$$T; S_1; S_2; S_3; S_4; D_1; D_2; D_3; D_4$$

as

$(A_7, A_6, A_5, A_4, A_3,$

$A_{15}, A_{14}, A_{13}, A_{12},$

$(0, A_{15}, A_{14}, A_{13}, A_{12},$

$(A_{15}, A_{14}, 0, 0, 0, A_{15},$

$(A_8, A_{13}, A_{15}, A_{14},$

$(A_{10}, A_8, 0, 0, 0, A_{15},$

$(A_{11}, A_9, 0, 0, A_{15},$

$(A_{12}, 0, A_{10}, A_9, A_{15},$

$(A_{13}, 0, A_{11}, A_{10}, A_{15},$

Compute $T + 2S_1$

$S_4 - D_1 - D_2 - D_3 - D_4$

Reduce modulo p

subtracting a few

“What a difference a prime makes”

NIST P-256 prime p is

$$2^{256} - 2^{224} + 2^{192} + 2^{96} - 1.$$

ECDSA standard specifies

reduction procedure given

an integer “ A less than p^2 ”:

Write A as

$$(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, A_{10}, A_9, \\ A_8, A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0),$$

meaning $\sum_i A_i 2^{32i}$.

Define

$$T; S_1; S_2; S_3; S_4; D_1; D_2; D_3; D_4$$

as

$$(A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0)$$

$$(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, 0, 0)$$

$$(0, A_{15}, A_{14}, A_{13}, A_{12}, 0, 0, 0)$$

$$(A_{15}, A_{14}, 0, 0, 0, A_{10}, A_9, A_8)$$

$$(A_8, A_{13}, A_{15}, A_{14}, A_{13}, A_{11},$$

$$(A_{10}, A_8, 0, 0, 0, A_{13}, A_{12}, A_{11},$$

$$(A_{11}, A_9, 0, 0, A_{15}, A_{14}, A_{13},$$

$$(A_{12}, 0, A_{10}, A_9, A_8, A_{15}, A_{14},$$

$$(A_{13}, 0, A_{11}, A_{10}, A_9, 0, A_{15},$$

$$\text{Compute } T + 2S_1 + 2S_2 + \\ S_4 - D_1 - D_2 - D_3 - D_4.$$

Reduce modulo p “by adding

subtracting a few copies” of

“What a difference a prime makes”

NIST P-256 prime p is

$$2^{256} - 2^{224} + 2^{192} + 2^{96} - 1.$$

ECDSA standard specifies

reduction procedure given

an integer “ A less than p^2 ”:

Write A as

$$(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, A_{10}, A_9, A_8, A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0),$$

meaning $\sum_i A_i 2^{32i}$.

Define

$$T; S_1; S_2; S_3; S_4; D_1; D_2; D_3; D_4$$

as

$$(A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0);$$

$$(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, 0, 0, 0);$$

$$(0, A_{15}, A_{14}, A_{13}, A_{12}, 0, 0, 0);$$

$$(A_{15}, A_{14}, 0, 0, 0, A_{10}, A_9, A_8);$$

$$(A_8, A_{13}, A_{15}, A_{14}, A_{13}, A_{11}, A_{10}, A_9);$$

$$(A_{10}, A_8, 0, 0, 0, A_{13}, A_{12}, A_{11});$$

$$(A_{11}, A_9, 0, 0, A_{15}, A_{14}, A_{13}, A_{12});$$

$$(A_{12}, 0, A_{10}, A_9, A_8, A_{15}, A_{14}, A_{13});$$

$$(A_{13}, 0, A_{11}, A_{10}, A_9, 0, A_{15}, A_{14}).$$

$$\text{Compute } T + 2S_1 + 2S_2 + S_3 + S_4 - D_1 - D_2 - D_3 - D_4.$$

Reduce modulo p “by adding or subtracting a few copies” of p .

"a difference a prime makes"

256 prime p is

$$2^{224} + 2^{192} + 2^{96} - 1.$$

standard specifies

n procedure given

er "A less than p^2 ":

as

$A_4, A_{13}, A_{12}, A_{11}, A_{10}, A_9,$

$A_6, A_5, A_4, A_3, A_2, A_1, A_0),$

$$\sum_i A_i 2^{32i}.$$

$S_2; S_3; S_4; D_1; D_2; D_3; D_4$

What is
Variable

$(A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0);$

$(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, 0, 0, 0);$

$(0, A_{15}, A_{14}, A_{13}, A_{12}, 0, 0, 0);$

$(A_{15}, A_{14}, 0, 0, 0, A_{10}, A_9, A_8);$

$(A_8, A_{13}, A_{15}, A_{14}, A_{13}, A_{11}, A_{10}, A_9);$

$(A_{10}, A_8, 0, 0, 0, A_{13}, A_{12}, A_{11});$

$(A_{11}, A_9, 0, 0, A_{15}, A_{14}, A_{13}, A_{12});$

$(A_{12}, 0, A_{10}, A_9, A_8, A_{15}, A_{14}, A_{13});$

$(A_{13}, 0, A_{11}, A_{10}, A_9, 0, A_{15}, A_{14}).$

Compute $T + 2S_1 + 2S_2 + S_3 + S_4 - D_1 - D_2 - D_3 - D_4.$

Reduce modulo p "by adding or subtracting a few copies" of $p.$

“a prime makes”

p is

$$+ 2^{96} - 1.$$

specifies

are given

than p^2 ”:

$A_{11}, A_{10}, A_9,$

$A_4, A_3, A_2, A_1, A_0),$

i .

$D_1; D_2; D_3; D_4$

What is “a few co

Variable-time loop

$(A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0);$

$(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, 0, 0, 0);$

$(0, A_{15}, A_{14}, A_{13}, A_{12}, 0, 0, 0);$

$(A_{15}, A_{14}, 0, 0, 0, A_{10}, A_9, A_8);$

$(A_8, A_{13}, A_{15}, A_{14}, A_{13}, A_{11}, A_{10}, A_9);$

$(A_{10}, A_8, 0, 0, 0, A_{13}, A_{12}, A_{11});$

$(A_{11}, A_9, 0, 0, A_{15}, A_{14}, A_{13}, A_{12});$

$(A_{12}, 0, A_{10}, A_9, A_8, A_{15}, A_{14}, A_{13});$

$(A_{13}, 0, A_{11}, A_{10}, A_9, 0, A_{15}, A_{14}).$

Compute $T + 2S_1 + 2S_2 + S_3 + S_4 - D_1 - D_2 - D_3 - D_4.$

Reduce modulo p “by adding or subtracting a few copies” of $p.$

makes"

L.

, A_9 ,

A_1, A_0),

; D_4

$(A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0);$
 $(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, 0, 0, 0);$
 $(0, A_{15}, A_{14}, A_{13}, A_{12}, 0, 0, 0);$
 $(A_{15}, A_{14}, 0, 0, 0, A_{10}, A_9, A_8);$
 $(A_8, A_{13}, A_{15}, A_{14}, A_{13}, A_{11}, A_{10}, A_9);$
 $(A_{10}, A_8, 0, 0, 0, A_{13}, A_{12}, A_{11});$
 $(A_{11}, A_9, 0, 0, A_{15}, A_{14}, A_{13}, A_{12});$
 $(A_{12}, 0, A_{10}, A_9, A_8, A_{15}, A_{14}, A_{13});$
 $(A_{13}, 0, A_{11}, A_{10}, A_9, 0, A_{15}, A_{14}).$

Compute $T + 2S_1 + 2S_2 + S_3 + S_4 - D_1 - D_2 - D_3 - D_4$.

Reduce modulo p "by adding or subtracting a few copies" of p .

What is "a few copies"?

Variable-time loop is unsafe.

$(A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0);$
 $(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, 0, 0, 0);$
 $(0, A_{15}, A_{14}, A_{13}, A_{12}, 0, 0, 0);$
 $(A_{15}, A_{14}, 0, 0, 0, A_{10}, A_9, A_8);$
 $(A_8, A_{13}, A_{15}, A_{14}, A_{13}, A_{11}, A_{10}, A_9);$
 $(A_{10}, A_8, 0, 0, 0, A_{13}, A_{12}, A_{11});$
 $(A_{11}, A_9, 0, 0, A_{15}, A_{14}, A_{13}, A_{12});$
 $(A_{12}, 0, A_{10}, A_9, A_8, A_{15}, A_{14}, A_{13});$
 $(A_{13}, 0, A_{11}, A_{10}, A_9, 0, A_{15}, A_{14}).$

Compute $T + 2S_1 + 2S_2 + S_3 + S_4 - D_1 - D_2 - D_3 - D_4$.

Reduce modulo p “by adding or subtracting a few copies” of p .

What is “a few copies”?

Variable-time loop is unsafe.

$(A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0);$
 $(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, 0, 0, 0);$
 $(0, A_{15}, A_{14}, A_{13}, A_{12}, 0, 0, 0);$
 $(A_{15}, A_{14}, 0, 0, 0, A_{10}, A_9, A_8);$
 $(A_8, A_{13}, A_{15}, A_{14}, A_{13}, A_{11}, A_{10}, A_9);$
 $(A_{10}, A_8, 0, 0, 0, A_{13}, A_{12}, A_{11});$
 $(A_{11}, A_9, 0, 0, A_{15}, A_{14}, A_{13}, A_{12});$
 $(A_{12}, 0, A_{10}, A_9, A_8, A_{15}, A_{14}, A_{13});$
 $(A_{13}, 0, A_{11}, A_{10}, A_9, 0, A_{15}, A_{14}).$

Compute $T + 2S_1 + 2S_2 + S_3 + S_4 - D_1 - D_2 - D_3 - D_4$.

Reduce modulo p “by adding or subtracting a few copies” of p .

What is “a few copies”?

Variable-time loop is unsafe.

Correct but quite slow:

conditionally add $4p$,

conditionally add $2p$,

conditionally add p ,

conditionally sub $4p$,

conditionally sub $2p$,

conditionally sub p .

$(A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0);$
 $(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, 0, 0, 0);$
 $(0, A_{15}, A_{14}, A_{13}, A_{12}, 0, 0, 0);$
 $(A_{15}, A_{14}, 0, 0, 0, A_{10}, A_9, A_8);$
 $(A_8, A_{13}, A_{15}, A_{14}, A_{13}, A_{11}, A_{10}, A_9);$
 $(A_{10}, A_8, 0, 0, 0, A_{13}, A_{12}, A_{11});$
 $(A_{11}, A_9, 0, 0, A_{15}, A_{14}, A_{13}, A_{12});$
 $(A_{12}, 0, A_{10}, A_9, A_8, A_{15}, A_{14}, A_{13});$
 $(A_{13}, 0, A_{11}, A_{10}, A_9, 0, A_{15}, A_{14}).$

Compute $T + 2S_1 + 2S_2 + S_3 + S_4 - D_1 - D_2 - D_3 - D_4$.

Reduce modulo p “by adding or subtracting a few copies” of p .

What is “a few copies”?

Variable-time loop is unsafe.

Correct but quite slow:

conditionally add $4p$,

conditionally add $2p$,

conditionally add p ,

conditionally sub $4p$,

conditionally sub $2p$,

conditionally sub p .

Delay until end of computation?

Trouble: “A less than p^2 ”.

$(A_7, A_6, A_5, A_4, A_3, A_2, A_1, A_0);$
 $(A_{15}, A_{14}, A_{13}, A_{12}, A_{11}, 0, 0, 0);$
 $(0, A_{15}, A_{14}, A_{13}, A_{12}, 0, 0, 0);$
 $(A_{15}, A_{14}, 0, 0, 0, A_{10}, A_9, A_8);$
 $(A_8, A_{13}, A_{15}, A_{14}, A_{13}, A_{11}, A_{10}, A_9);$
 $(A_{10}, A_8, 0, 0, 0, A_{13}, A_{12}, A_{11});$
 $(A_{11}, A_9, 0, 0, A_{15}, A_{14}, A_{13}, A_{12});$
 $(A_{12}, 0, A_{10}, A_9, A_8, A_{15}, A_{14}, A_{13});$
 $(A_{13}, 0, A_{11}, A_{10}, A_9, 0, A_{15}, A_{14}).$

Compute $T + 2S_1 + 2S_2 + S_3 + S_4 - D_1 - D_2 - D_3 - D_4$.

Reduce modulo p “by adding or subtracting a few copies” of p .

What is “a few copies”?

Variable-time loop is unsafe.

Correct but quite slow:

conditionally add $4p$,

conditionally add $2p$,

conditionally add p ,

conditionally sub $4p$,

conditionally sub $2p$,

conditionally sub p .

Delay until end of computation?

Trouble: “A less than p^2 ”.

Even worse: what about platforms where 2^{32} isn't best radix?

$(A_5, A_4, A_3, A_2, A_1, A_0);$
 $(A_4, A_{13}, A_{12}, A_{11}, 0, 0, 0);$
 $(A_{14}, A_{13}, A_{12}, 0, 0, 0);$
 $(A_4, 0, 0, 0, A_{10}, A_9, A_8);$
 $(A_4, A_{15}, A_{14}, A_{13}, A_{11}, A_{10}, A_9);$
 $(A_4, 0, 0, 0, A_{13}, A_{12}, A_{11});$
 $(A_4, 0, 0, A_{15}, A_{14}, A_{13}, A_{12});$
 $(A_{10}, A_9, A_8, A_{15}, A_{14}, A_{13});$
 $(A_{11}, A_{10}, A_9, 0, A_{15}, A_{14}).$

$e T + 2S_1 + 2S_2 + S_3 +$
 $- D_2 - D_3 - D_4.$

modulo p “by adding or
 subtracting a few copies” of p .

What is “a few copies”?
 Variable-time loop is unsafe.

Correct but quite slow:

conditionally add $4p$,
 conditionally add $2p$,
 conditionally add p ,
 conditionally sub $4p$,
 conditionally sub $2p$,
 conditionally sub p .

Delay until end of computation?

Trouble: “ A less than p^2 ”.

Even worse: what about platforms
 where 2^{32} isn't best radix?

There are many cryptographic algorithms that
 affect different platforms differently.
 correct or incorrect?
 e.g. ECDSA
 of scalar multiplication
 e.g. ECDSA
 additions
 EdDSA

$(A_3, A_2, A_1, A_0);$
 $(A_2, A_{11}, 0, 0, 0);$
 $(A_{12}, 0, 0, 0);$
 $(A_{10}, A_9, A_8);$
 $(A_{13}, A_{11}, A_{10}, A_9);$
 $(A_{13}, A_{12}, A_{11});$
 $(A_{14}, A_{13}, A_{12});$
 $(A_8, A_{15}, A_{14}, A_{13});$
 $(A_9, 0, A_{15}, A_{14}).$

$+ 2S_2 + S_3 +$
 $D_3 - D_4.$

“by adding or
 copies” of p .

What is “a few copies”?
 Variable-time loop is unsafe.

Correct but quite slow:

conditionally add $4p$,
 conditionally add $2p$,
 conditionally add p ,
 conditionally sub $4p$,
 conditionally sub $2p$,
 conditionally sub p .

Delay until end of computation?

Trouble: “ A less than p^2 ”.

Even worse: what about platforms
 where 2^{32} isn't best radix?

There are many m
 cryptographic desi
 affect difficulty of
 correct constant-ti
 e.g. ECDSA needs
 of scalars. EdDSA
 e.g. ECDSA splits
 additions into sever
 EdDSA uses comp

A_0);
 $, 0)$;
 $)$;
 $)$;
 $A_{10}, A_9)$;
 $1)$;
 $A_{12})$;
 $, A_{13})$;
 $A_{14})$.
 $S_3 +$

g or
 p .

What is “a few copies”?
Variable-time loop is unsafe.

Correct but quite slow:

conditionally add $4p$,

conditionally add $2p$,

conditionally add p ,

conditionally sub $4p$,

conditionally sub $2p$,

conditionally sub p .

Delay until end of computation?

Trouble: “ A less than p^2 ”.

Even worse: what about platforms
where 2^{32} isn't best radix?

There are many more ways to
cryptographic design choices
affect difficulty of building fast
correct constant-time software.

e.g. ECDSA needs divisions
of scalars. EdDSA doesn't.

e.g. ECDSA splits elliptic-curve
additions into several cases.

EdDSA uses complete formulas.

What is “a few copies”?

Variable-time loop is unsafe.

Correct but quite slow:

conditionally add $4p$,

conditionally add $2p$,

conditionally add p ,

conditionally sub $4p$,

conditionally sub $2p$,

conditionally sub p .

Delay until end of computation?

Trouble: “ A less than p^2 ”.

Even worse: what about platforms
where 2^{32} isn't best radix?

There are many more ways that
cryptographic design choices
affect difficulty of building fast
correct constant-time software.

e.g. ECDSA needs divisions
of scalars. EdDSA doesn't.

e.g. ECDSA splits elliptic-curve
additions into several cases.

EdDSA uses complete formulas.

What is “a few copies”?

Variable-time loop is unsafe.

Correct but quite slow:

conditionally add $4p$,

conditionally add $2p$,

conditionally add p ,

conditionally sub $4p$,

conditionally sub $2p$,

conditionally sub p .

Delay until end of computation?

Trouble: “ A less than p^2 ”.

Even worse: what about platforms where 2^{32} isn't best radix?

There are many more ways that cryptographic design choices affect difficulty of building fast correct constant-time software.

e.g. ECDSA needs divisions of scalars. EdDSA doesn't.

e.g. ECDSA splits elliptic-curve additions into several cases.

EdDSA uses complete formulas.

What's better use of time:

implementing ECDSA, or

upgrading protocol to EdDSA?